

# Powering of Future HEP Detectors in 4T & High Radiation - Commercial solutions?

Satish K Dhawan  
Yale University



Technical Seminar at TRIUMF  
August 5, 2010

1 Oodle = 10,000 amps

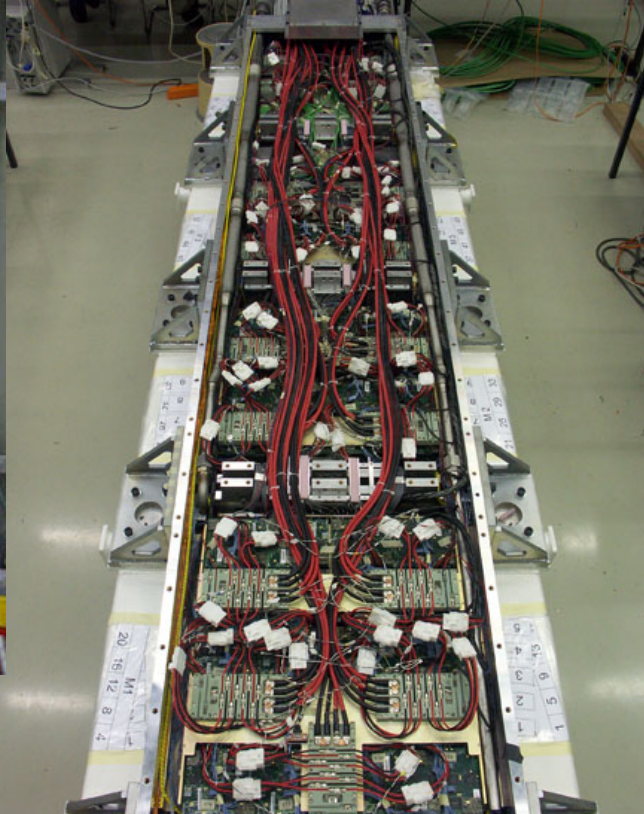
# Agenda

- ❖ CMS ECAL Powering 2.5 V @ 50,000 amps
- ❖ DC-DC Converters: *Reduce Power Supply Currents*
- ❖ Commercial Device 100 Mrads- Beginners luck
- ❖ ATLAS Upgrade
- ❖ Air Coil , Noise tests
- ❖ Why Thin Oxide for Radiation
- ❖ End of Silicon for Power
- ❖ GaN Wide band Gap material. RF & Power Switching
- ❖ Industry Developments 400V DC distribution
- ❖ Did we find a commercial part for sLHC ?
- ❖ **Market Trends** Single Chip
- ❖ LHC Beam Power Supplies
- ❖ Advantage of this development
- ❖ Conclusions

## Collaborators:

*Yale University:* Keith Baker, Hunter Smith

*Brookhaven National Laboratory:* Hucheng Chen, James Kierstead, Francesco Lanni, David Lynn, Sergio Rescia,



11 Regulators each with Output Current maximum = 2.5 amps

Power Input  
4.3 Volt Analog  
4.3 Volt Digital

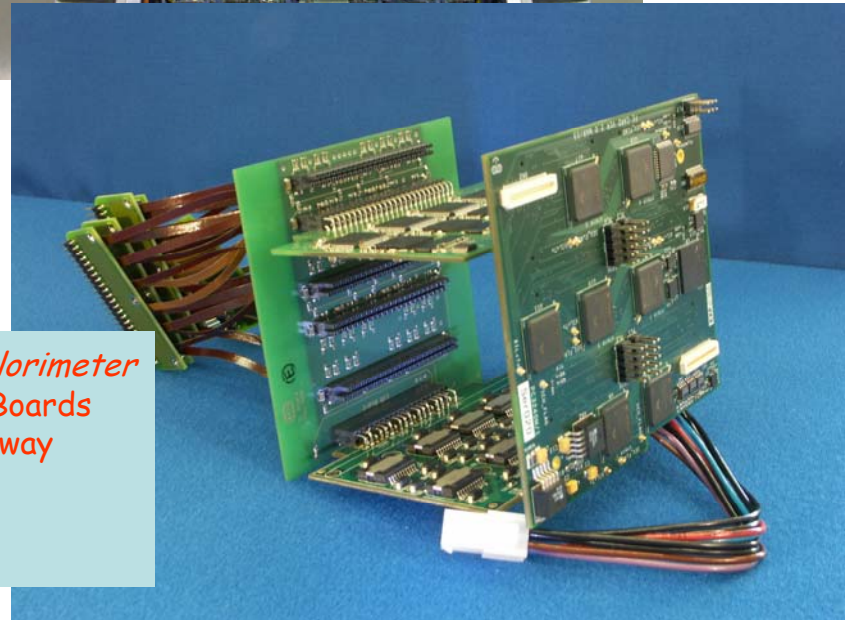
Operating Current = 16 amps

Motherboard for VFE Cards

FE card

Thermistors (Total = 3)

*CMS ECAL: Electromagnetic Calorimeter*  
 80 Amps Power supply for 4 LVR Boards  
 Power Supply @6.3V 30 meters away  
 3K Boards x 16 amps = 48 Kamps  
 Magnetic Field 4T in CMS  
 Power Delivery Efficiency < 40 %



LVR: Low Voltage

37 Countries, 155 Institutes, 2000 scientists (including about 400 students)    October 2006

**TRIGGER, DATA ACQUISITION & OFFLINE COMPUTING**

Austria, Brazil, CERN, Finland, France, Greece, Hungary, Ireland, Italy, Korea, Poland, Portugal, Switzerland, UK, USA

**TRACKER**

Austria, Belgium, CERN, Finland, France, Germany, Italy, Japan\*, Mexico, New Zealand, Switzerland, UK, USA

**CRYSTAL ECAL**

Belarus, CERN, China, Croatia, Cyprus, France, Italy, Japan\*, Portugal, Russia, Serbia, Switzerland, UK, USA

**PRESHOWER**

Armenia, CERN, Greece, India, Russia, Taiwan

**RETURN YOKE**

Barrel: Czech Rep., Estonia, Germany, Greece, Russia  
Endcap: Japan\*, USA

**SUPERCONDUCTING MAGNET**

All countries in CMS contribute to Magnet financing in particular: Finland, France, Italy, Japan\*, Korea, Switzerland, USA

**HCAL**

Barrel: Bulgaria, India, Spain\*, USA  
Endcap: Belarus, Bulgaria, Georgia, Russia, Ukraine, Uzbekistan  
HO: India

**FEET**

Pakistan  
China

**FORWARD CALORIMETER**

Hungary, Iran, Russia, Turkey, USA

**MUON CHAMBERS**

Barrel: Austria, Bulgaria, CERN, China, Germany, Hungary, Italy, Spain,  
Endcap: Belarus, Bulgaria, China, Colombia, Korea, Pakistan, Russia, USA

\* Only through industrial contracts

**Total weight** : 12500 T  
**Overall diameter** : 15.0 m  
**Overall length** : 21.5 m  
**Magnetic field** : 4 Tesla

**CMS ECAL: 5 Oodles (50 Kamps) .**

Power Supply output = 315 KW  
Power loss in Leads to SM = 100 KW  
Power loss in Regulator Card = 90 KW  
Power Delivered @ 2.5 V = 125 KW

1 Oodle = 10,000 amps

Power Supply  
6.3 V

64 Amps

30 m

# of Power Supplies ~ 700

# of ST LDO Chips = 35 K LHC Radiation Hard made by ST Microelectronics

# of LVR Cards = 3.1 K.

**Yale: Designed, built, burn-in and Tested.**

Vdrop = 2V  
Pd = 128 W

2x16 mm<sup>2</sup> (AWG 6)

1 to 3 m

50 mm<sup>2</sup> (AWG 00)

SM: Super Module

4.3 V

Junction Box

2.5V

64 amps

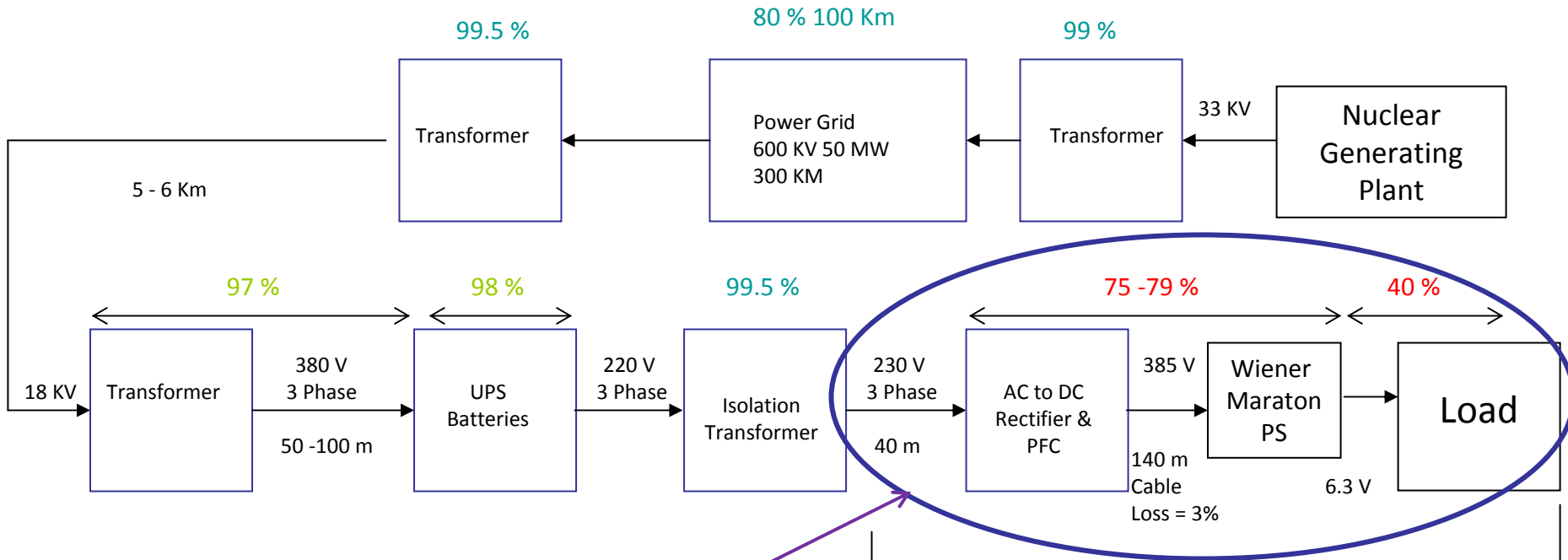
160 W

4 LVR Boards

**Power Delivery Efficiency = 40%  
NOT INCLUDED**

1. Power Supply efficiency
2. Water cooling
3. Removal of Waste heat
4. Air Conditioning

# Power Chain Efficiency for CMS ECAL



*Represents the efficiency of power delivery to a physics detector, e.g. ECal*

From Experts Efficiency %

Guess work Efficiency %

**Power delivery Efficiency = 30 %**

**with Power for Heat Removal = 20 %**

# What can we do?

- Is there a better way to distribute power ?
- High Radiation
- Magnetic Field 4 T
- Load ~1 V Oodles of current
- Feed High Voltage and Convert - *like AC power transmission*
- Commercial Technologies — *No Custom ASIC Chips*
- Learn from Semiconductor Industry
- Use Company Evaluation Boards for testing

# Type of High to Low Voltage Converters without transformers

## ❖ Charge pumps

- Normally limited to integral fractions of input voltage
- Losses proportional to switch losses
- Can provide negative voltage

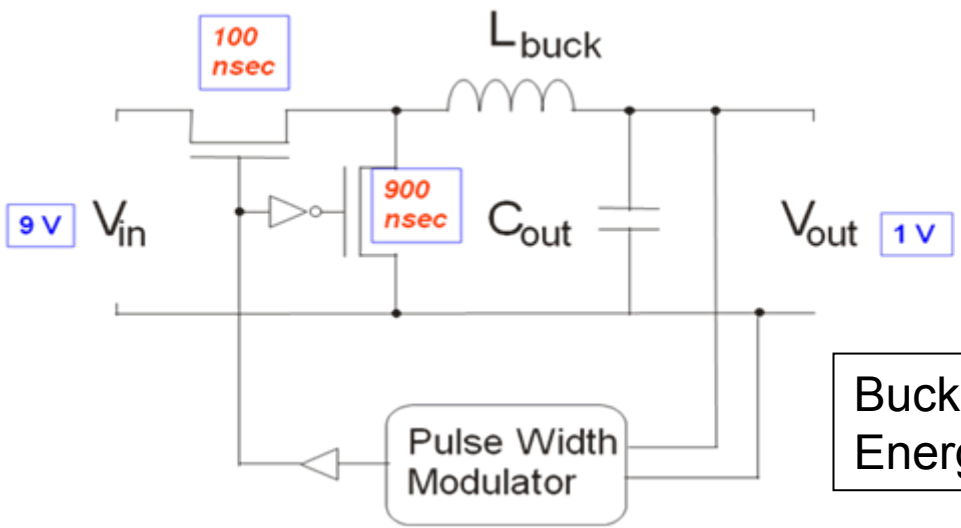
## ❖ Buck Converter – Used in consumer & Industrial Electronics

- Needs an ASIC, Inductor and Capacitors
- Cannot provide a negative voltage
- Topology allows for more flexibility in output voltage than charge pump
- Much more common use in commercial applications





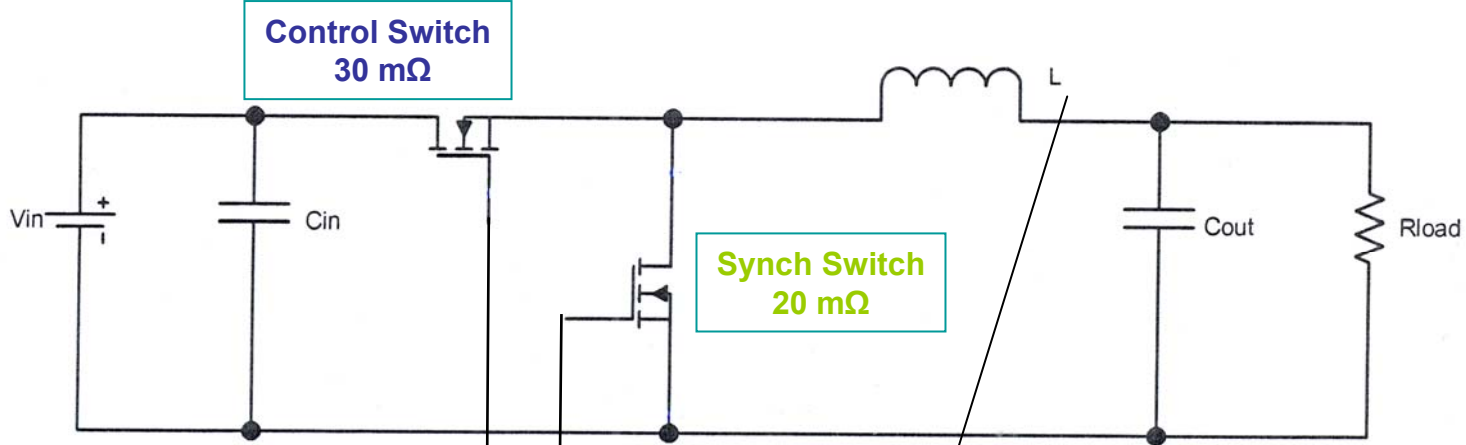
Charge Pump: Charge capacitors in series & discharge in parallel



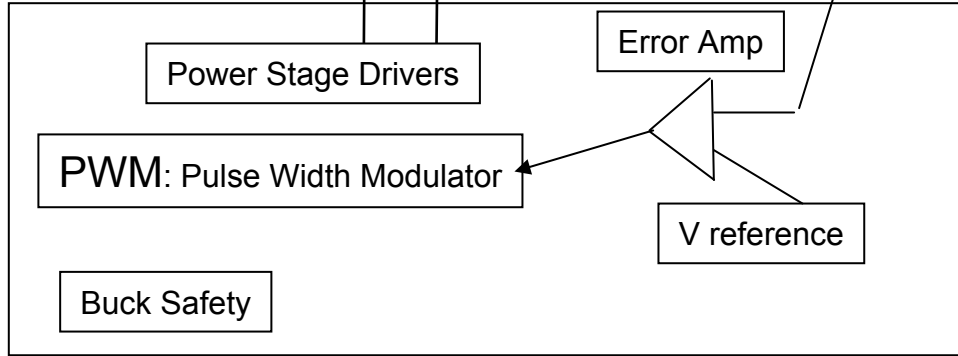
Buck Converter  
Energy Stored in an inductor

# Synchronous Buck Converter

Power Stage  
-  
High Volts

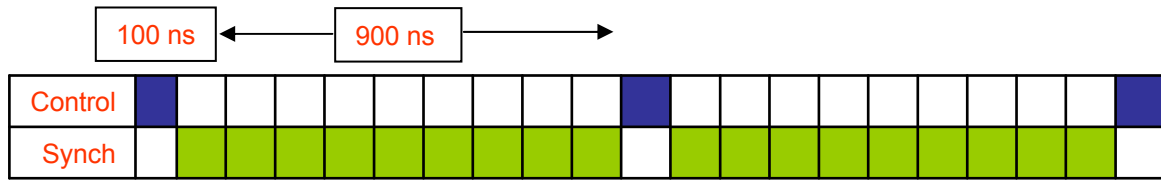


Controller  
Low Voltage



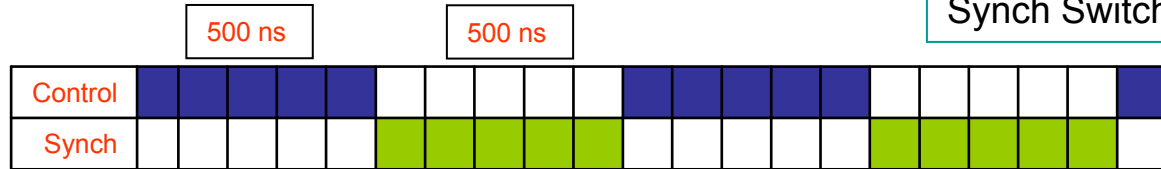
Minimum Switch ON Time  
Limits Max Frequency  
10 nsec @ 10 MHz

Vout = 11%

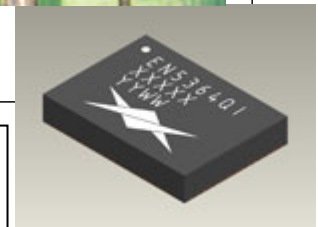
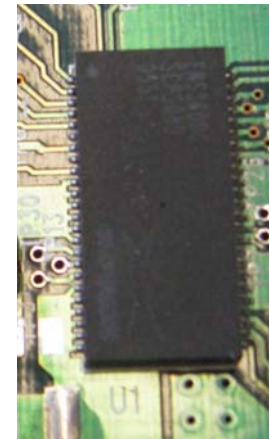
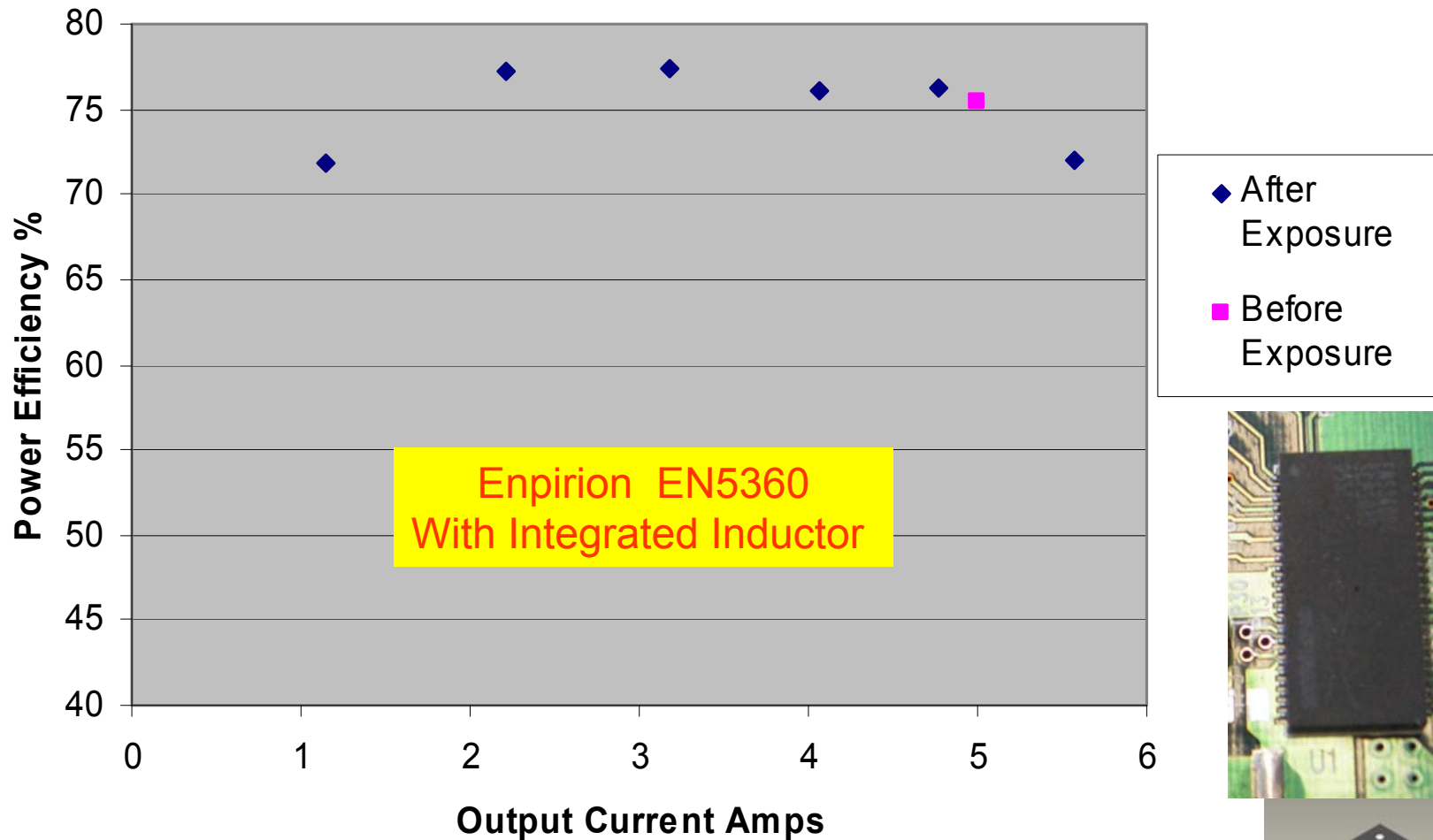


Control Switch: Switching Loss > I<sup>2</sup>  
Synch Switch: R<sub>ds</sub> Loss Significant

Vout = 50%



## Buck Regulator Efficiency after 100 Mrad dosage

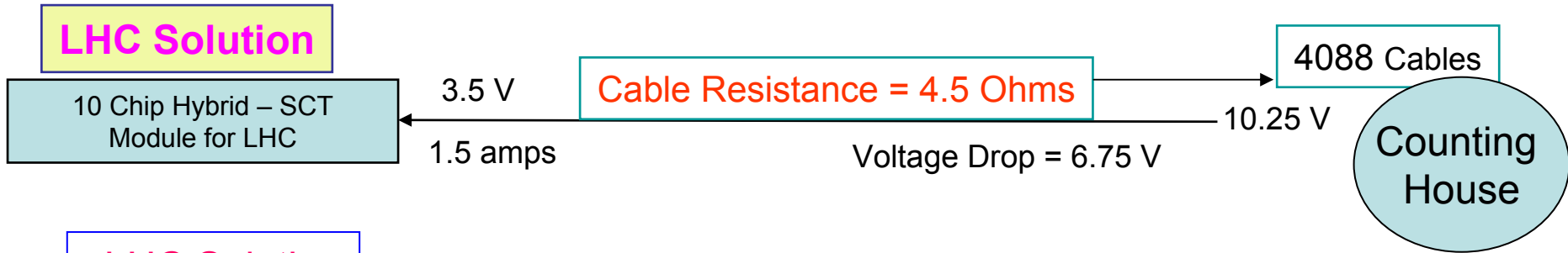


Found out at Power Technology conference 0.25  $\mu\text{m}$  Lithography

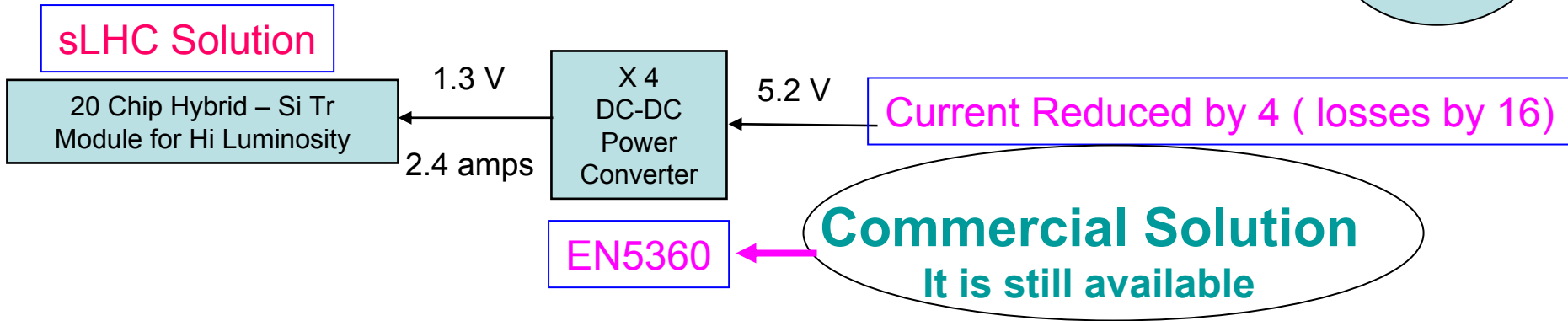
- Irradiated Stopped on St. Valentines Day 2007
- We reported @ TWEPP 2008 - IHP was foundry for EN5360

Length of Power Cables = 140 Meters

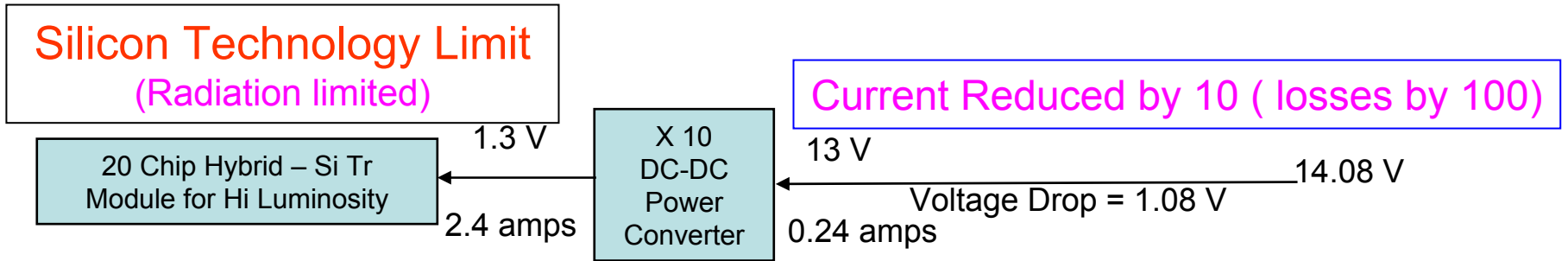
LHC Solution



sLHC Solution



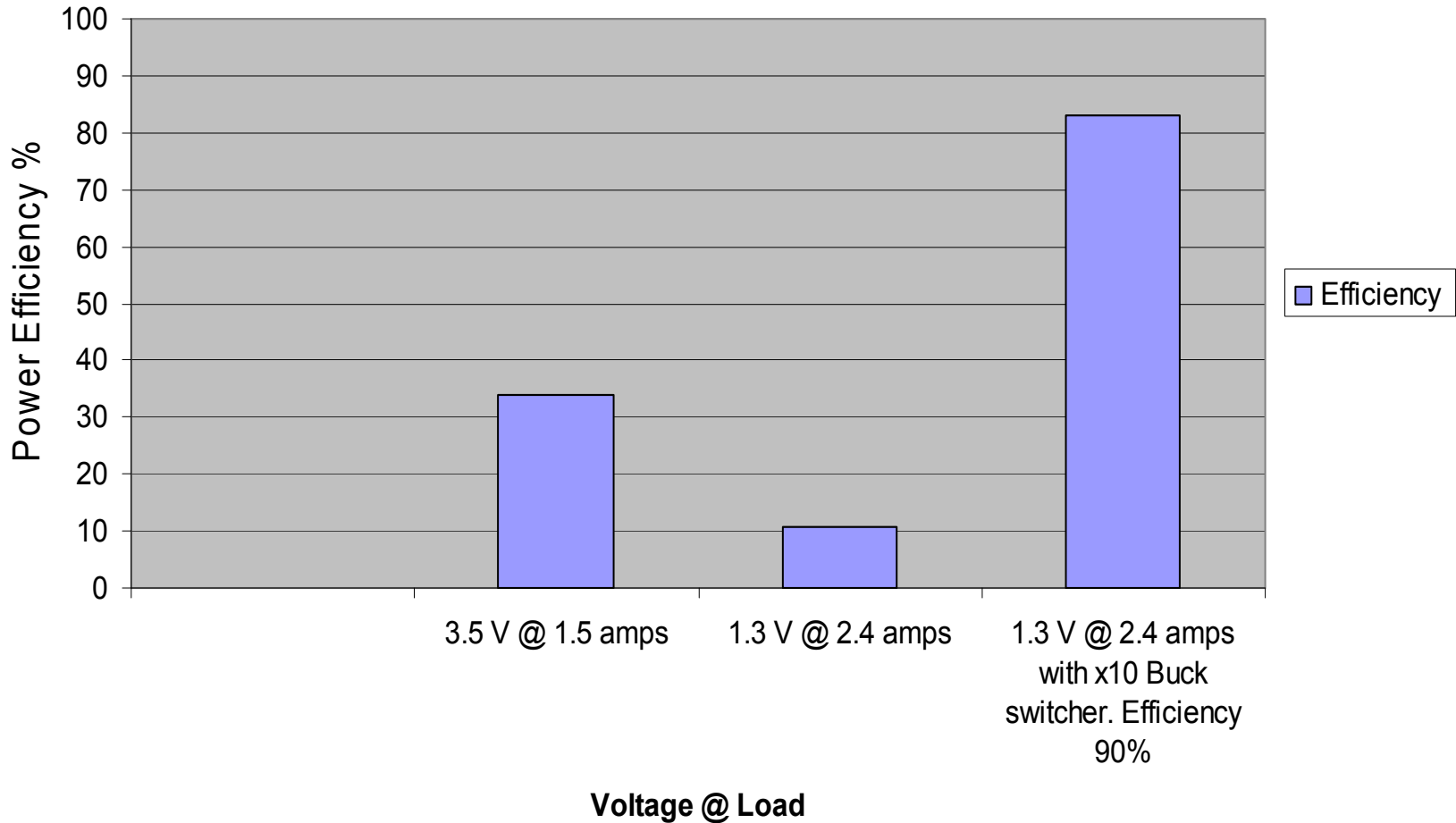
Silicon Technology Limit (Radiation limited)



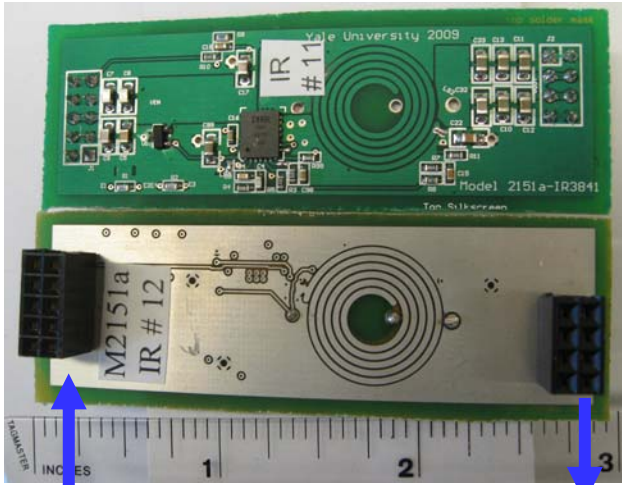
> X 40 with Gallium Nitride Transistors

## Power Delivery with Existing SCT Cables (total = 4088)

Resistance = 4.5 Ohms



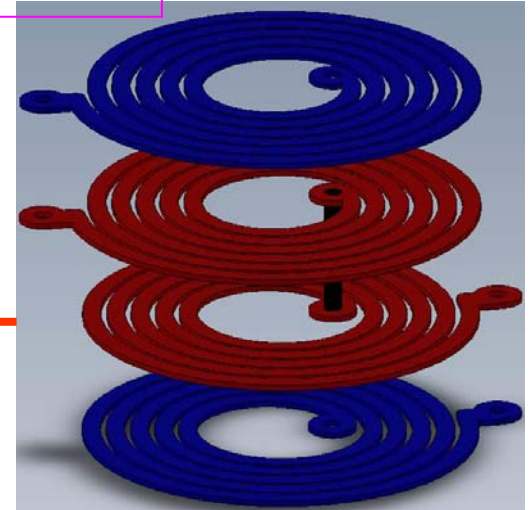
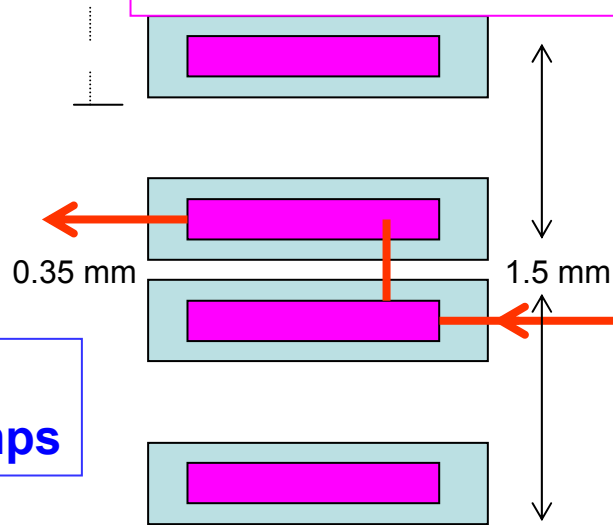
# Plug In Card with Shielded Buck Inductor



12 V

2.5 V  
@ 6 amps

Coupled Air Core Inductor  
Connected in Series



Spiral Coils Resistance in mΩ

	Top	Bottom
3 Oz PCB	57	46
0.25 mm Cu Foil	19.4	17

## Different Versions

### ❖ Converter Chips

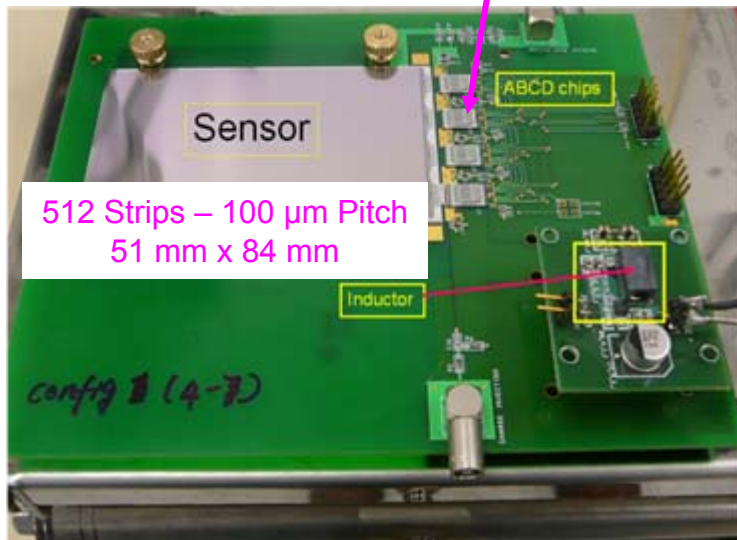
Max8654 monolithic  
IR8341 3 die MCM

### ❖ Coils

Embedded 3oz cu  
Solenoid 15 mΩ  
Spiral Etched 0.25mm

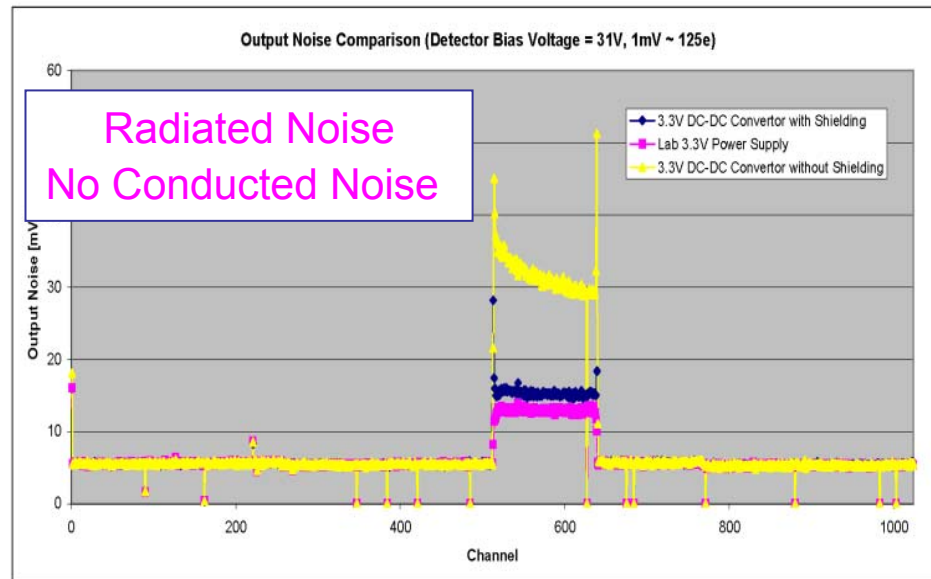
Test @ BNL

Only One Chip Bonded

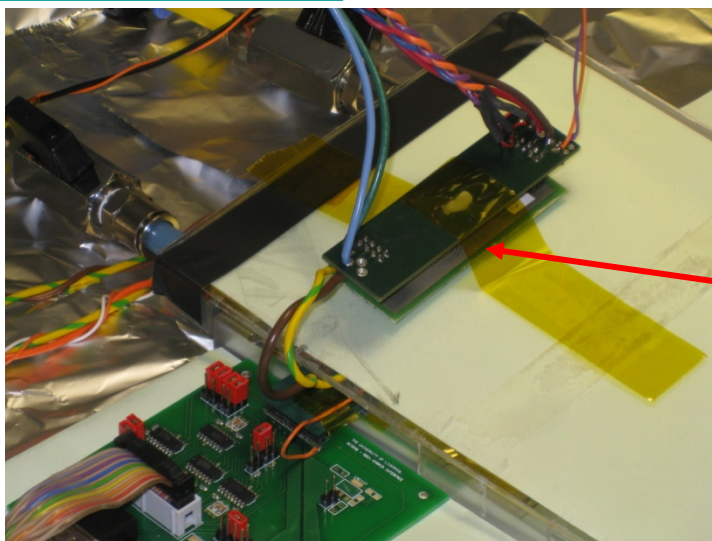


512 Strips – 100  $\mu$ m Pitch  
51 mm x 84 mm

# Noise Tests with Silicon Sensors



Test @ Liverpool

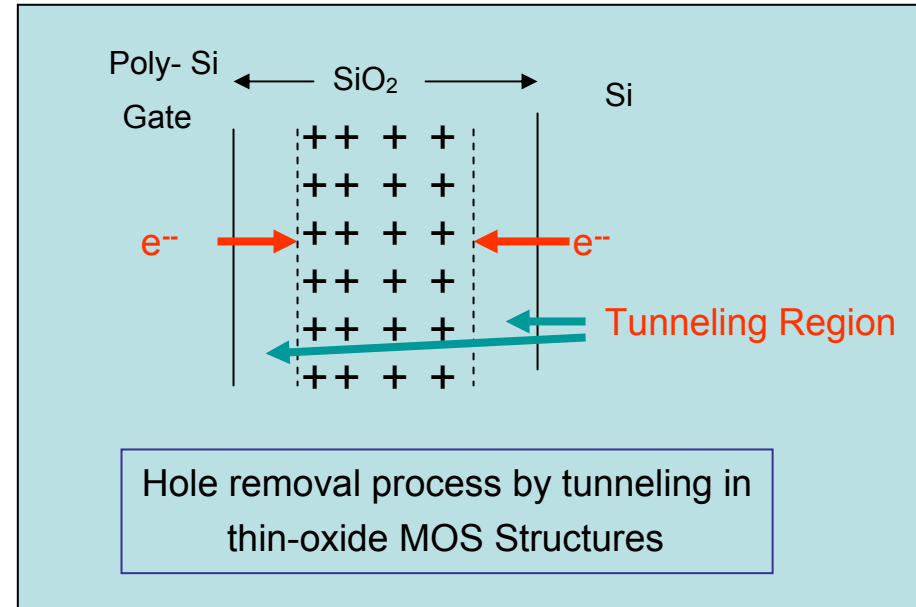
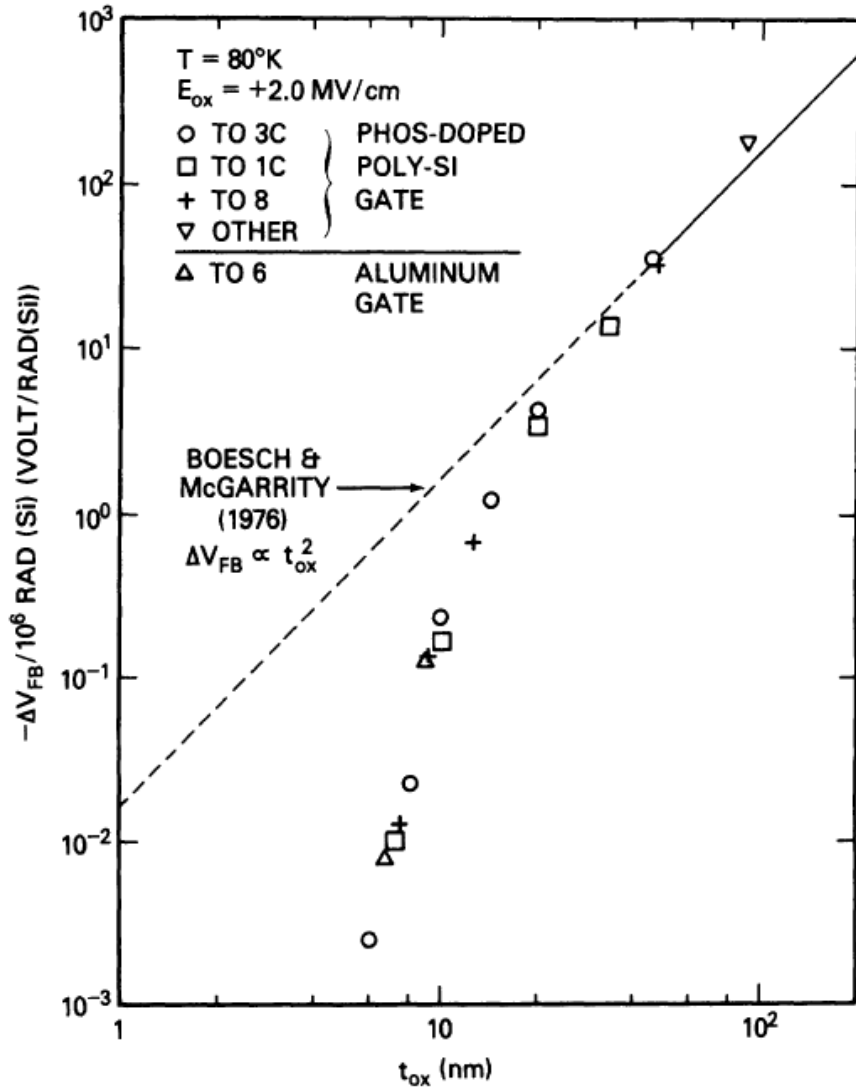


Plug in Card  
1 cm from Coil  
facing Sensor

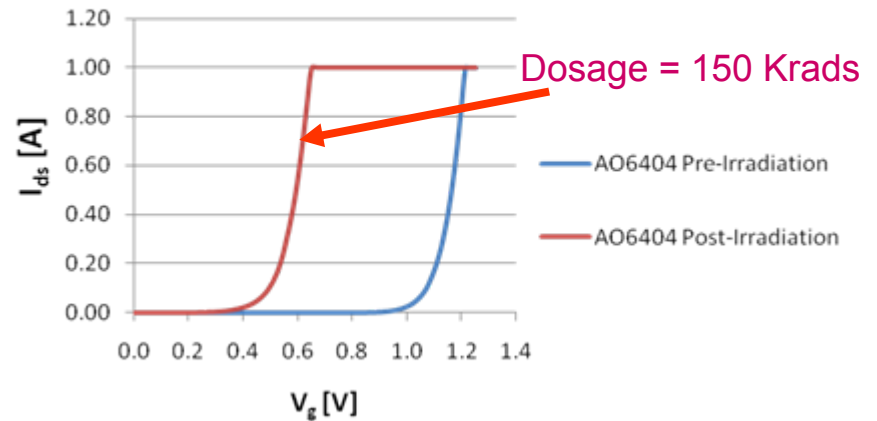
20  $\mu$ m Al foil  
shielding

Coil Type	Power	Input Noise electrons rms
Solenoid	DC - DC	881
Solenoid	Linear	885
Spiral Coil	DC - DC	666
Spiral Coil	Linear	664

# Threshold Shift vs Gate Oxide Thickness



Shifting  $V_t$  of MOSFET With Gammas



Sachs et. al. IEEE Trans. Nuclear Science NS-31, 1249 (1984)

Book. Timothy R Oldham "Ionizing Radiation Effects in MOS Oxides" 1999 World Scientific



# CERN ASICs

Mantra: Deep sub micron is more rad hard

Why ?

IBM Foundry Oxide Thickness			
Lithography	Process	Operating	Oxide
	Name	Voltage	Thickness
			nm
0.25 $\mu\text{m}$	6SF	2.5	5
		3.3	7
0.13 $\mu\text{m}$	8RF	1.2 & 1.5	2.2
		2.2 & 3.3	5.2

# Can We Have High Radiation Tolerance & Higher Voltage Together ???

Higher radiation tolerance needs thin oxide  
while higher voltage needs thicker oxide – Contradiction ?

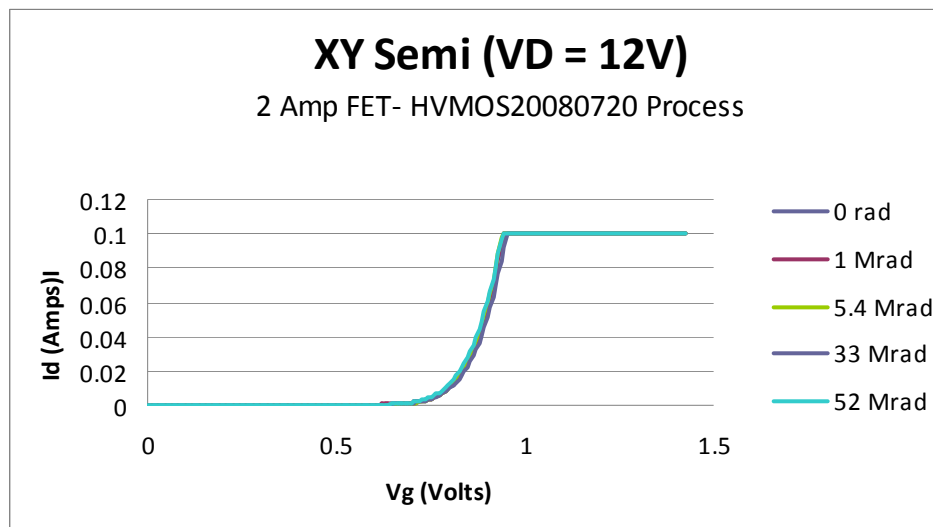
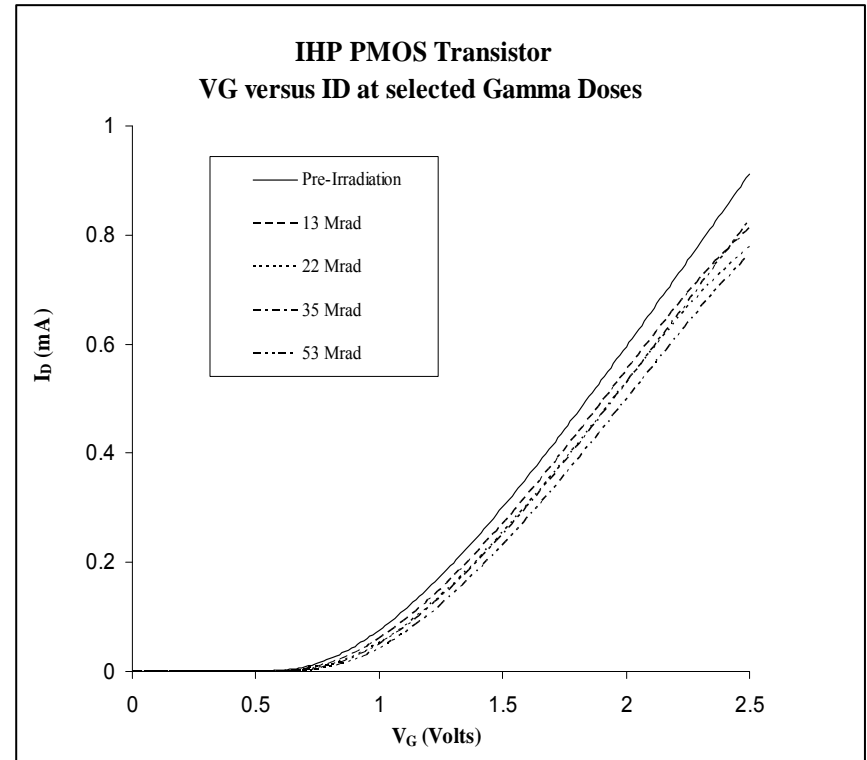
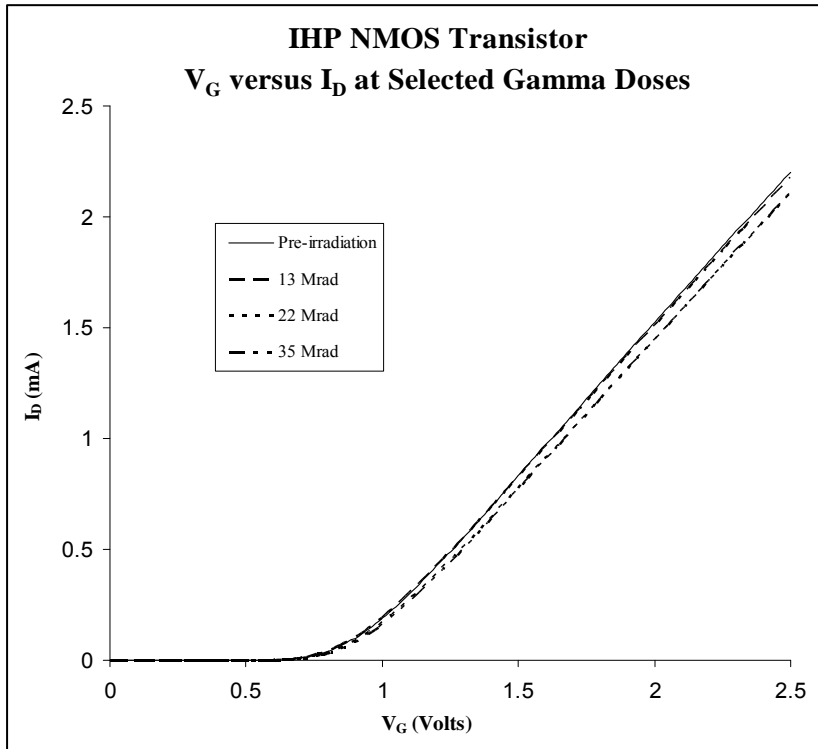
Mixed signal power designs from TI, TSMC, IBM etc - 0.18  $\mu\text{m}$  & 0.13  $\mu\text{m}$   
Automobile Market. Voltage ratings 10 - 80 Volts  
Deep sub-micron but thick oxide

Controller : Low Voltage

High Voltage: Switches – some candidates HV & Thin oxide

LDMOS, Drain Extension, Deep Diffusion etc

>> 20 Volts HEMT GaN on Silicon, Silicon Carbide, Sapphire



## Thin Oxide Devices (non IBM)

Company	Device	Process	Foundry	Oxide	Dose before	Observation
		Name/ Number	Name	nm	Damage seen	Damage Mode
IHP	ASIC custom	SG25V GOD <b>12 V</b>	IHP, Germany	5		Minimal Damage
XySemi	FET 2 amps	HVMOS20080720 <b>12 V</b>	China	7		Minimal Damage
XySemi	XP2201	HVMOS20080720 <b>15 V</b>	China	12 / 7		2Q2010
Enpirion	EN5365	CMOS 0.25 $\mu$ m	Dongbu HiTek, Korea	5	64 Krads	
Enpirion	EN5382	CMOS 0.25 $\mu$ m	Dongbu HiTek, Korea	5	111 Krads	
Enpirion	EN5360 #2	SG25V (IHP)	IHP, Germany	5	100 Mrads	Minimal Damage
Enpirion	EN5360 #3	SG25V (IHP)	IHP, Germany	5	48 Mrads	Minimal Damage

Necessary condition for Radiation Hardness - **Thin Gate Oxide**

***But not sufficient***

IHP: Epi free, High resistivity substrate, Higher voltage, lower noise devices

Dongbu: Epi process on substrate, lower voltage due to hot carriers in gate oxide

# Gallium Nitride Devices Tests 2009

## **RF GaN** 20 Volts & 0.1 amp

- ❖ 8 pieces: Nitronex NPT 25015: [GaN on Silicon](#)
- ✓ Done Gamma, Proton & Neutrons
- ✓ 65 volts Oct 2009
  
- ❖ 2 pieces: CREE CGH40010F: [GaN on siC](#)
  
- ❖ 6 pieces: Eudyna EGNB010MK: [GaN on siC](#)
- ✓ Done Neutrons

## **Switch GaN**

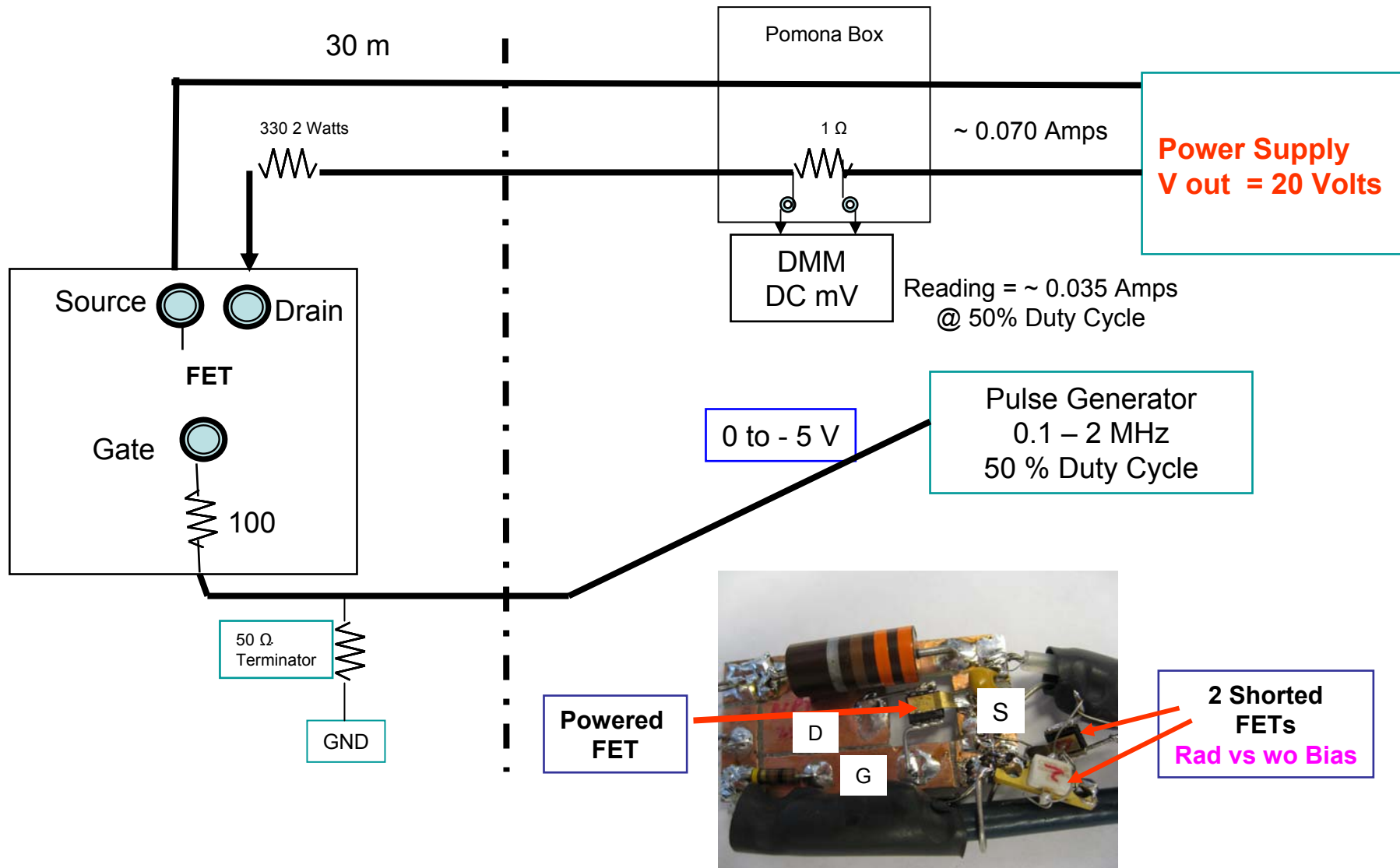
- ❖ International Rectifier [GaN on Silicon](#)  
[Under NDA](#)

Gamma: @ BNL  
Protons: @ Lansce  
Neutrons: @ U of Mass Lowell

Expose same device to  
Gamma, Protons & Neutrons  
Online Monitoring

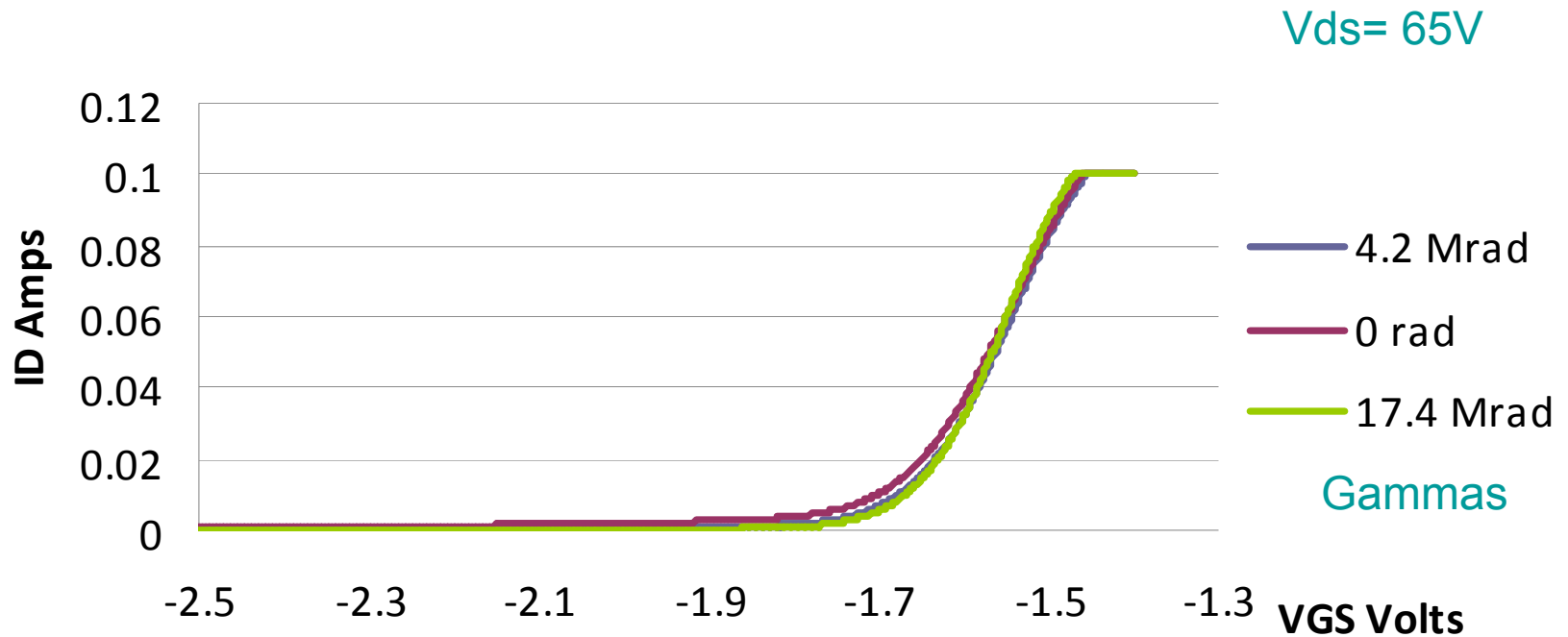
# Bias during Radiation

## Max operating V & I Limit Power by duty cycle



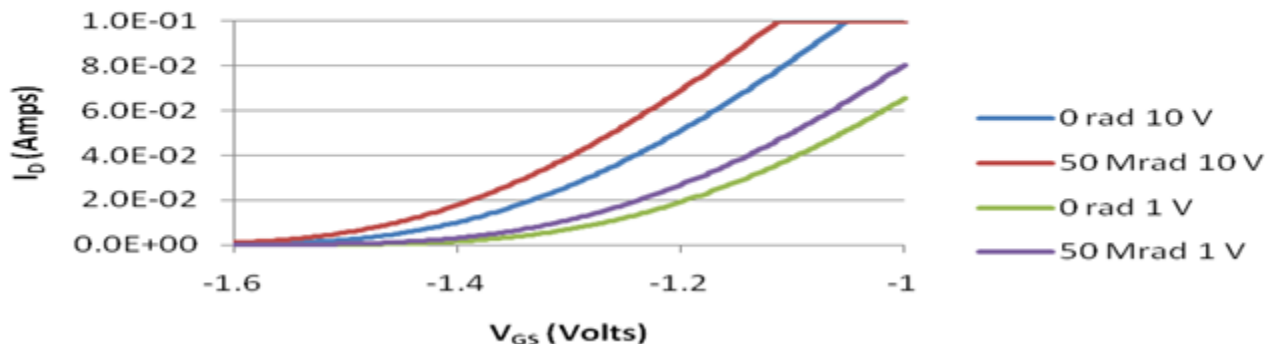
# Nitronex 25015

Serial # 1



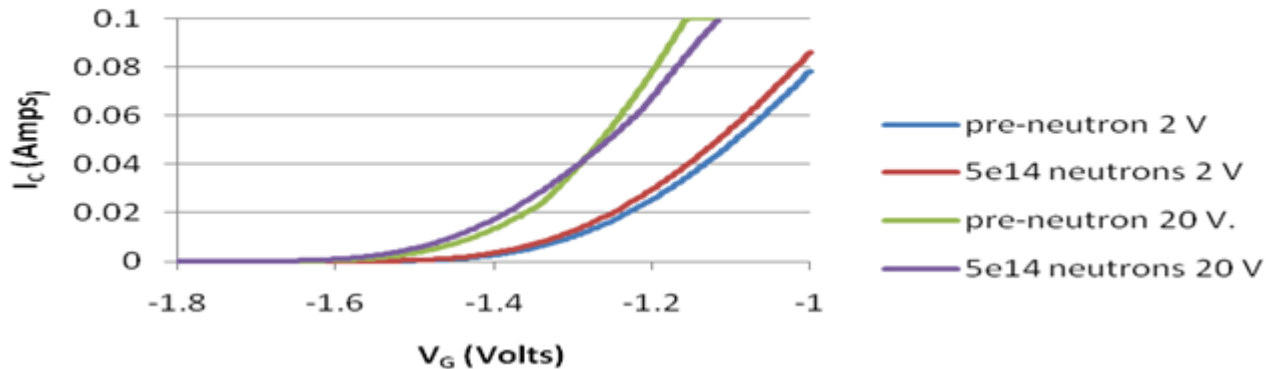
200 Mrads of Protons had no effect – switching 20 V 0.1 Amp  
Parts still activated after 7 months

### Eudyna GaN HEMT Before and After Gamma Irradiation $V_{ds} = 1 \text{ V}, 10 \text{ V}$



Our next IEEE TNS  
Paper shall summarize  
work to date

### Eudyna GaN HEMT Before and After Neutron Irradiation



### Proton Test

Proton Fluence =  $1 \times 10^{15} \text{ p/cm}^2$  over a period of about 24 hours.

Biased = 65 volts switching @ 1MHz

Average current = 65 mA limited by Load resistor . No change in current.



# GaN for Power Switching

- High frequency ~ 10 GHz
- Low  $R_{ds}$  & low gate charge > High FOM: Figure of Merit
- High Thermal Conductivity & x10 higher Dielectric strength than Silicon
- No Gate Dielectric !

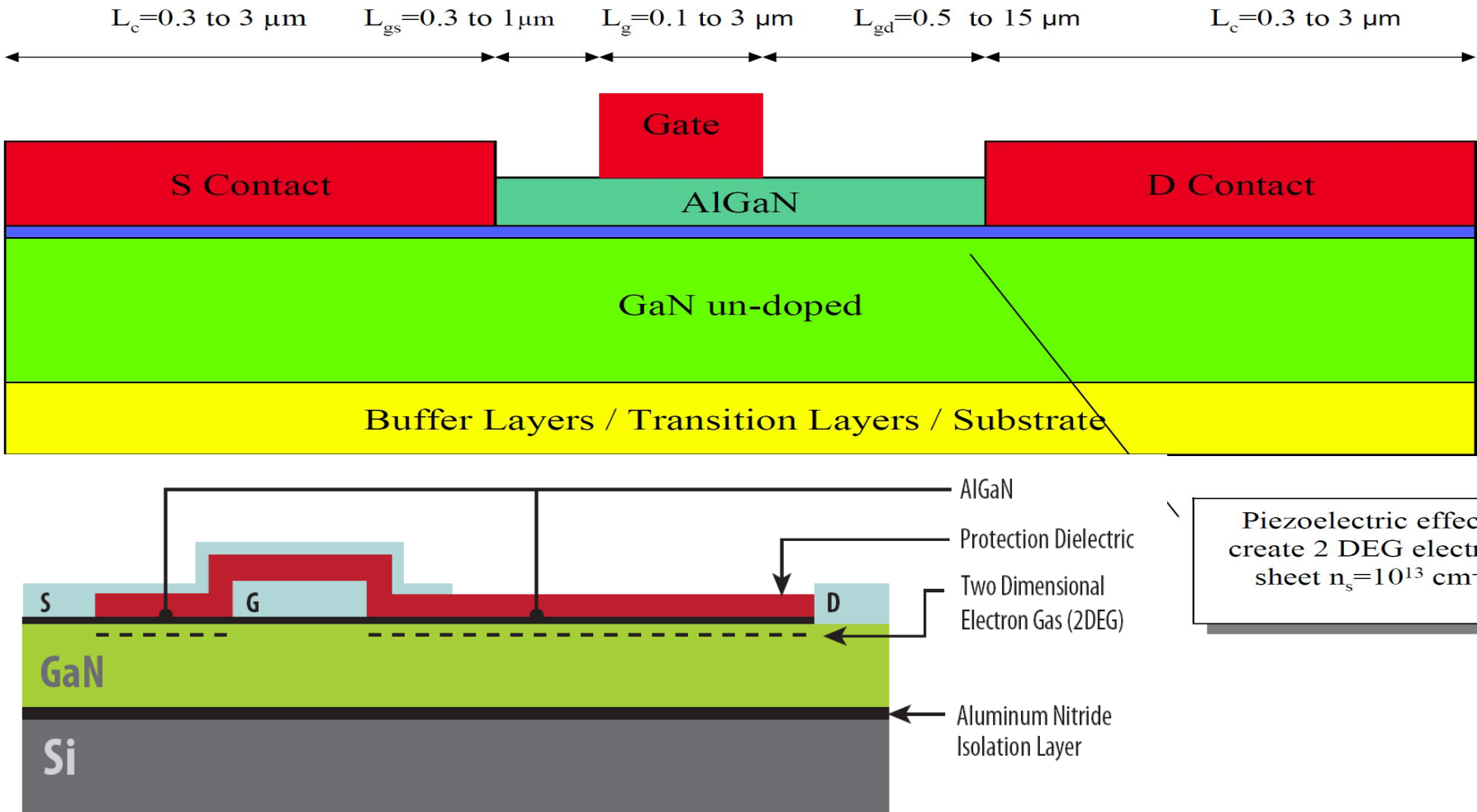


Figure 1: GaN on silicon devices have a very simple structure similar to a lateral DMOS device and can be built in a standard CMOS foundry

# Gallium Nitride Power Devices Tests 2010

## Power Devices

- ❖ Efficient Power Conversion Corp (EPC) GaN on Silicon  
5 Devices 40 - 200 Volts 3- 33 amps. *Sold by Digikey*  
600 V Device samples December 2010
- ❖ International Rectifier GaN on Silicon. Announced Feb 2010  
Not yet available for little people with no pockets

## Irradiation

Organized by BNL & Yale

Gamma: @ BNL Aug – Sept 2010

Protons: @ Lansce August 2010

Neutrons: @ U of Mass Lowell ??

## Irradiation

Organized by Sandia & Yale

Gamma: @ Sandia \$

Protons: @ TRIUMF September 2010 \$

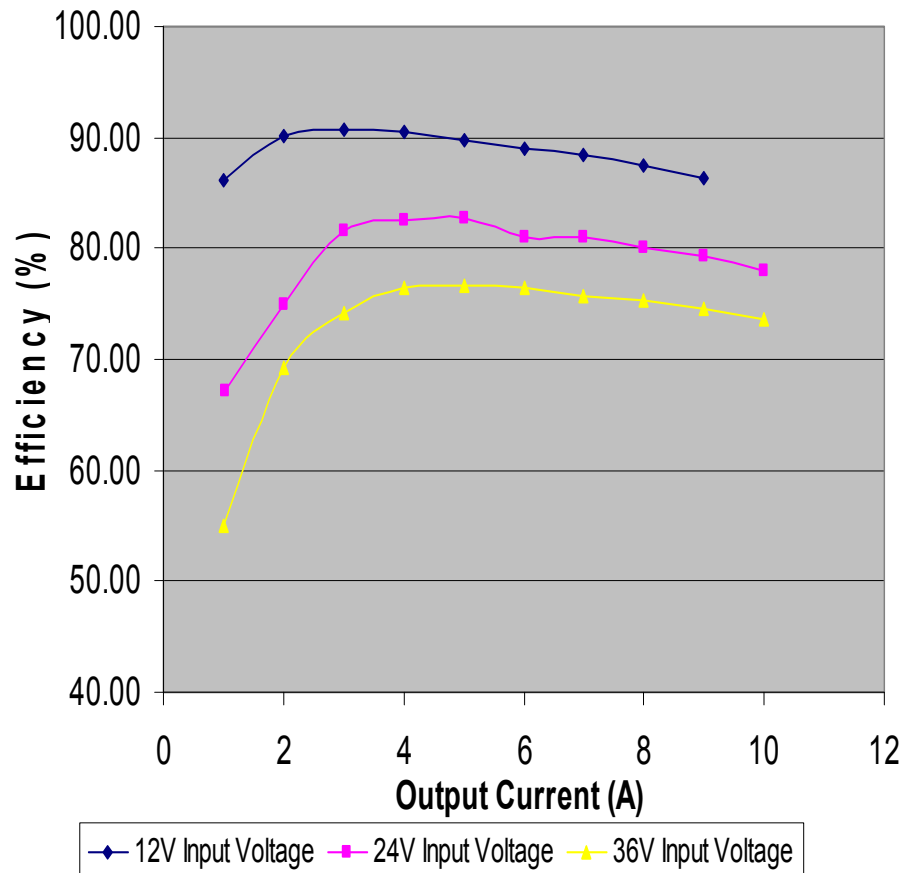
Heavy Ions: @ TAMU August 2010

: epc 1015 – 40V: Efficiency with constant frequency and constant on pulse with inputs of 12, 24 & 36 Volts.

### EPC9001 #2 Efficiency vs Output Current

Constant Frequency = 566 KHz: Pulse width =124 - 240 ns:

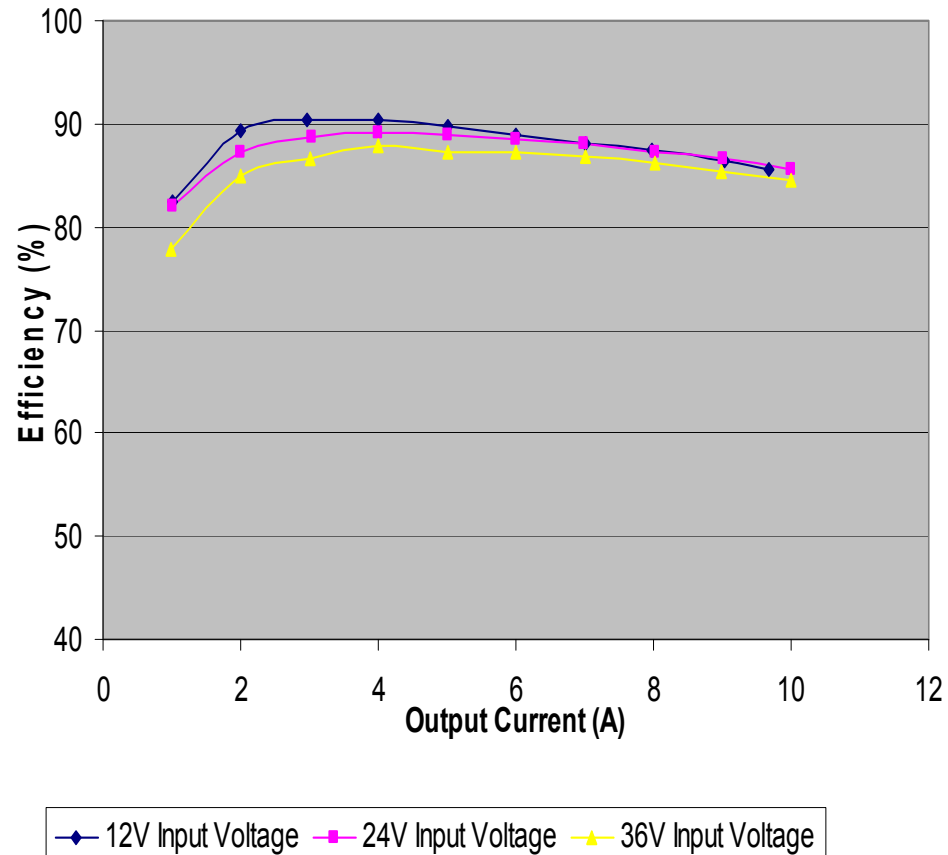
Vout = 0.95 -1.34V: L= 3.9  $\mu$ H, 4.8 m $\Omega$



### EPC9001 #2 Efficiency vs Output Current

Constant twd = 240 ns: Frequency = 164 - 568 kHz

Vout ~1.2V: L = 3.9  $\mu$ H, 4.8 m $\Omega$

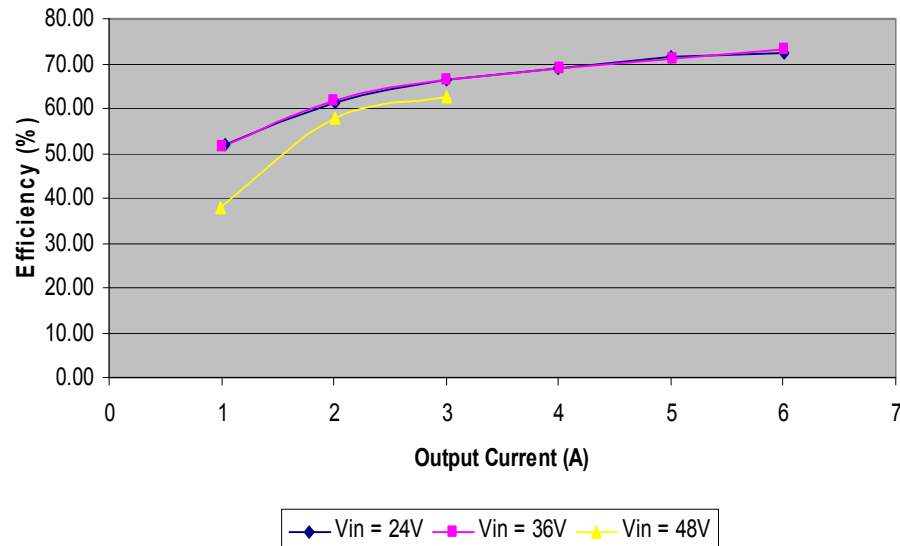


: epc 1001 – 100V: Efficiency with 2 constant frequencies. Inputs of 24, 36 & 48 Volts.

### EPC9002 #1 Efficiency vs Output Current

Constant Frequency = 496 kHz: Pulse width =100 - 173 ns:

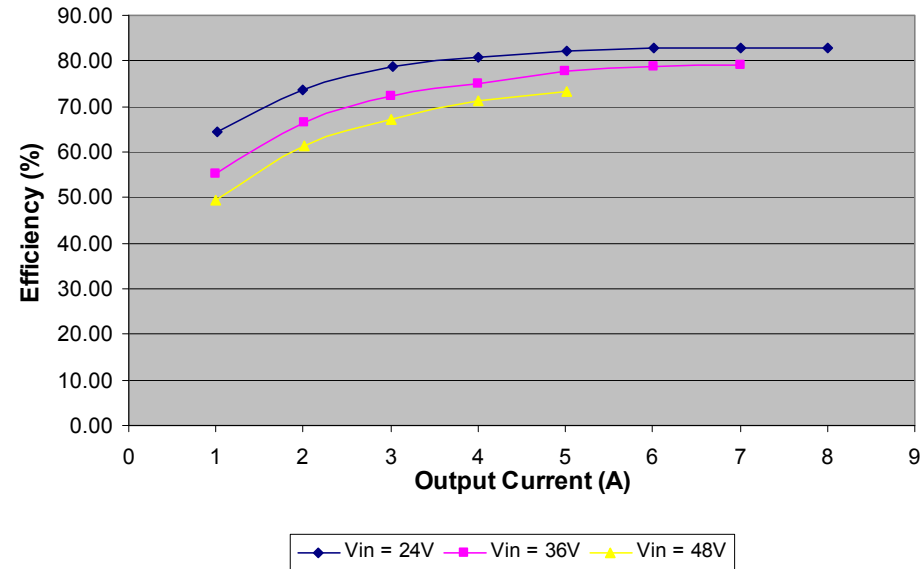
Vout = 1.2015 -1.857.V: L = 3.9  $\mu$ H: R= 4.8 m $\Omega$



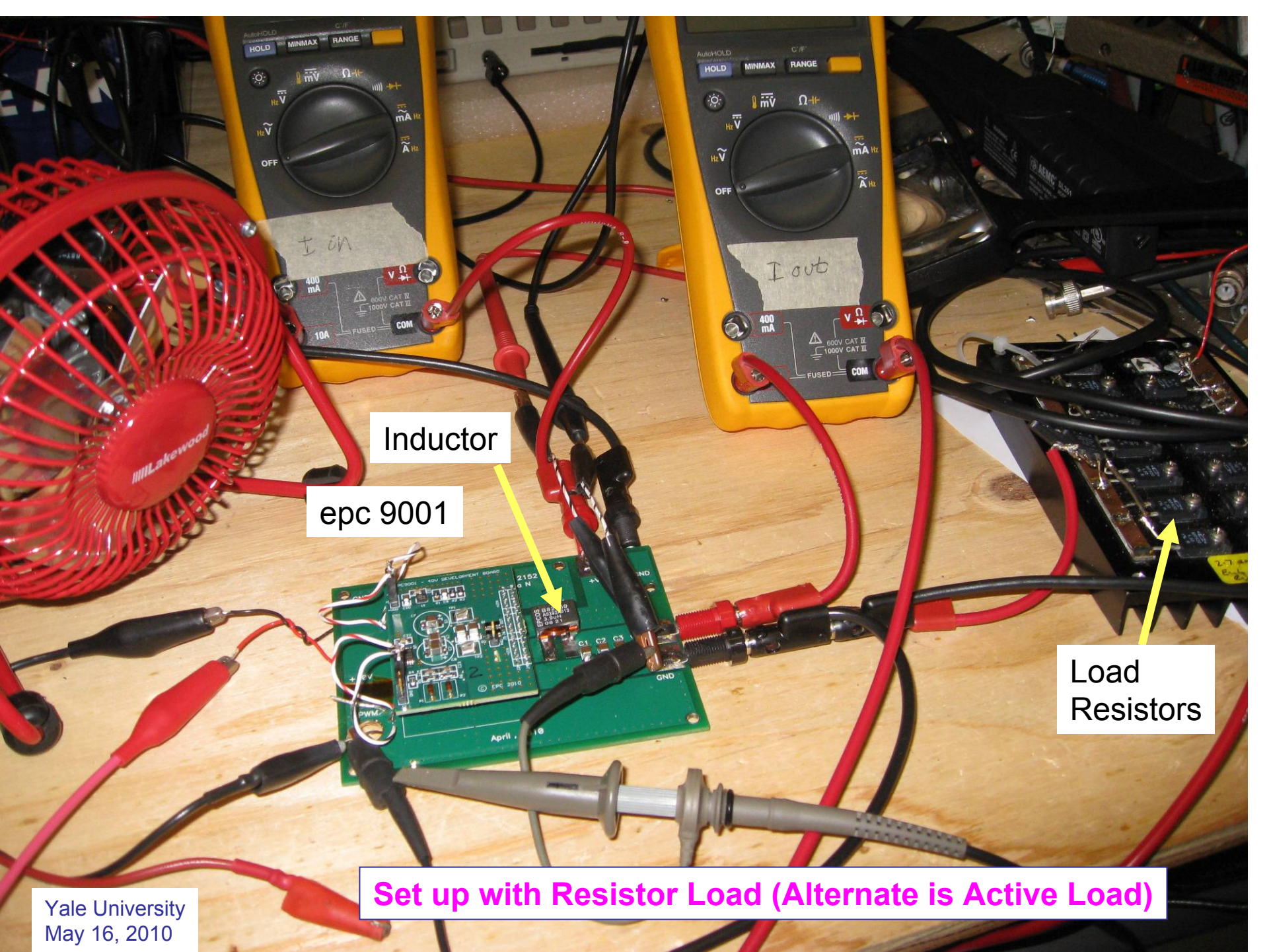
### EPC9002 #1 Efficiency vs Output Current

Constant Frequency = 266 kHz: Pulse width =166 - 358 ns:

Vout = 1.7984 -1.8144.V: L = 3.9  $\mu$ H: R= 4.8 m $\Omega$



Need better GaN drivers preferably integrated on the wafer

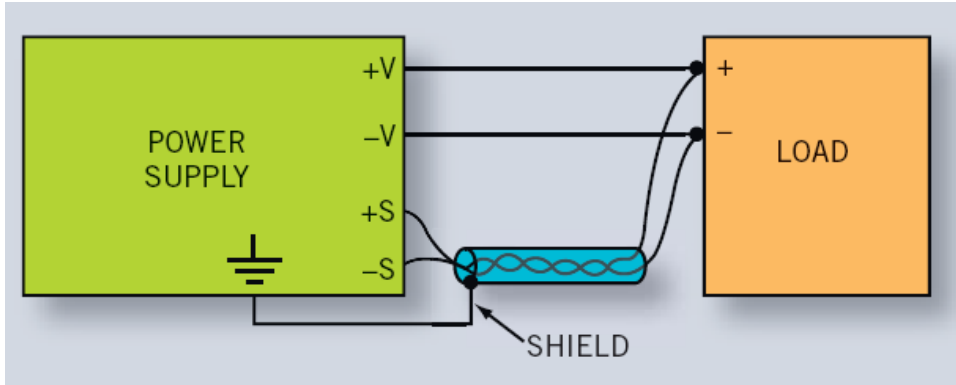
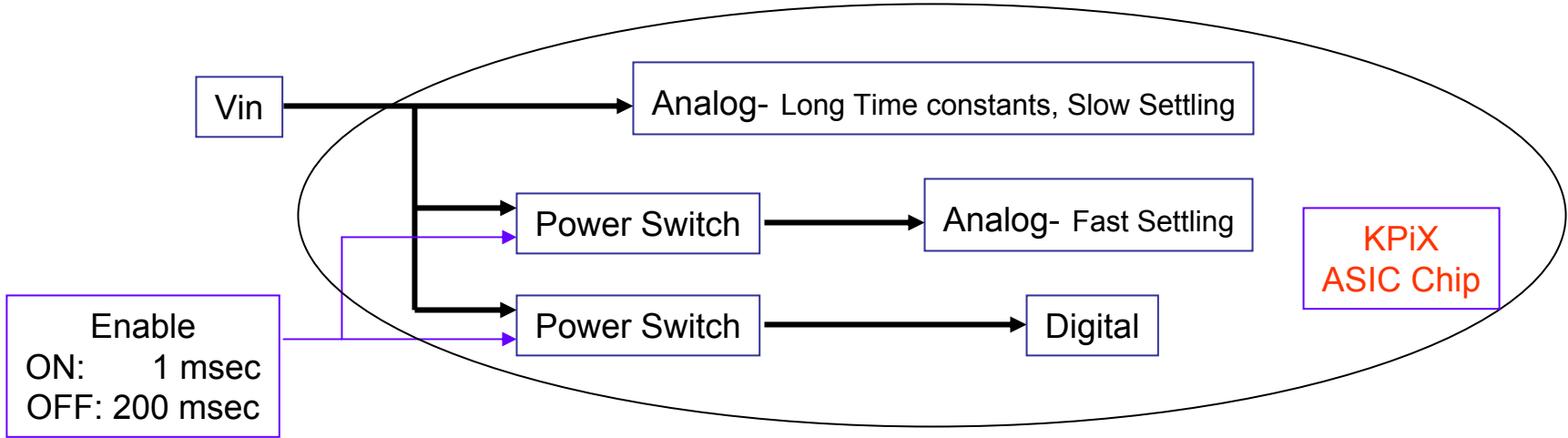


Inductor

epc 9001

Load Resistors

Set up with Resistor Load (Alternate is Active Load)



# Powering ILC SiD Detector

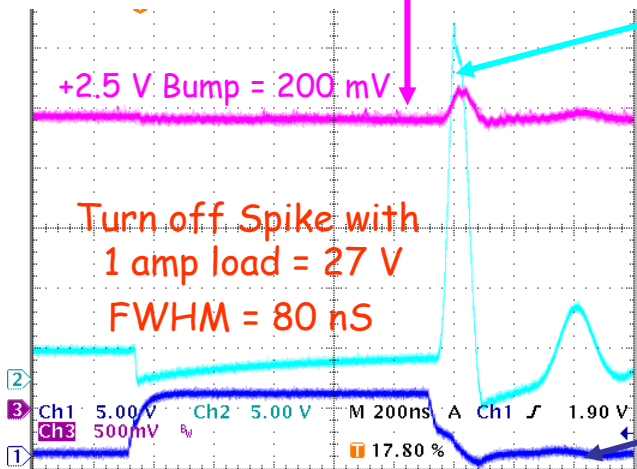
# Pulsing Load

Air Coil DC-DC Converter

Vin 12 V

Plug in card  
Maxim / IR

3 meters Twisted pair AWG 24



Load Resistor  
2.5 Ω 10 W

Gate  
+5V

Enable Gate  
ON: 0.8 μs  
OFF: 10 μs

Gnd

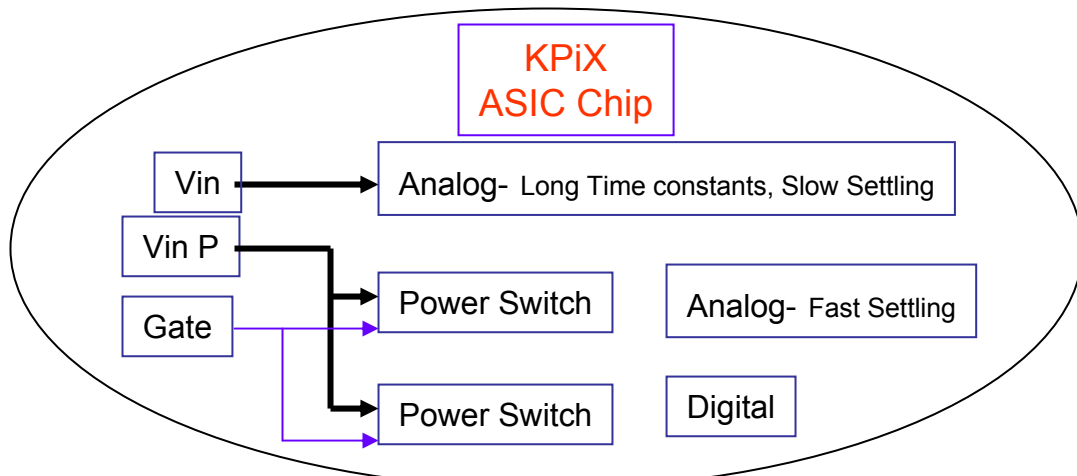
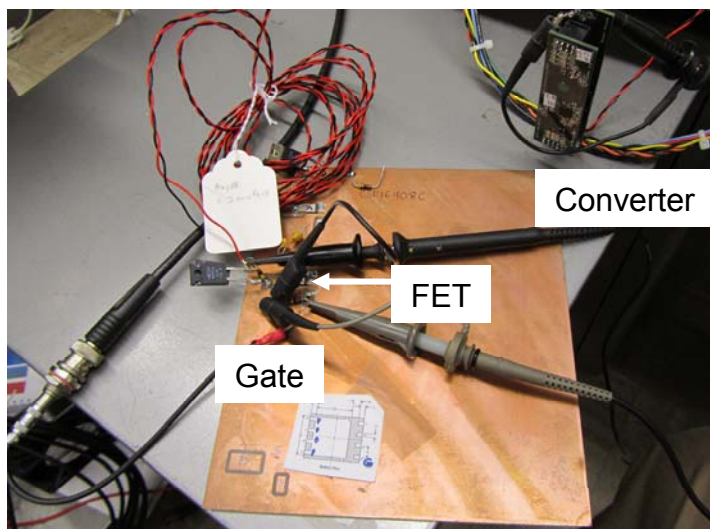
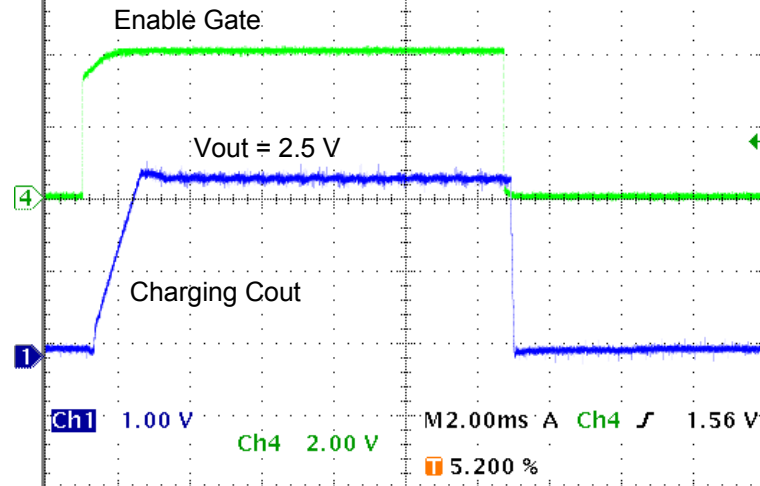
# Pulsing Converter

Air Coil DC-DC Converter

Tek Run

Trig'd

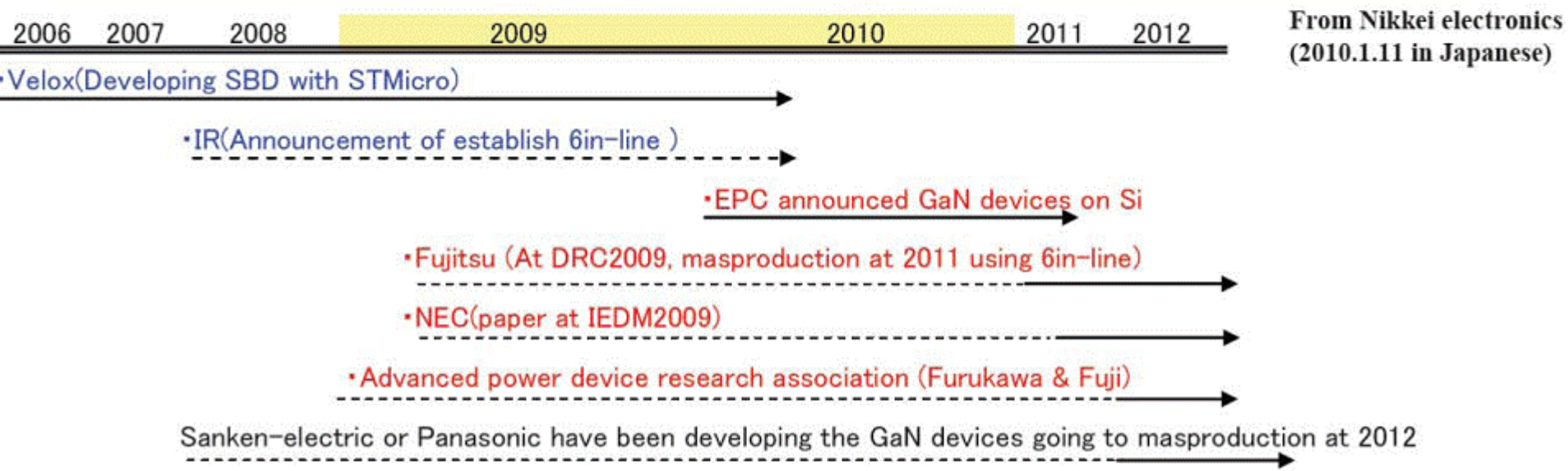
Load = 3 amps (Electronic)



# SiD Powering Pulsing

# Status of GaN player

Company	Detail of Target or status
Fujitsu Laboratory	Mass-production level in 2011(fiscal)~2012 in the medium Vb over 600V using Si or SiC substrate (representative by Fujitsu Micro-elect.)
Furukawa and Fuji Electric	Commercial use at 2011(fiscal)
International Rectifiers	Commercial use from 2010 Beginning of product is lower Vb such several tens of voltage
NEC (Renesus)	Deliver Sample at 2011(fiscal)
Panasonic	Commercial use at 2011(fiscal)
Rohm	Deliver Sample at 2011(fiscal), also developing GaN native substrate
Sanken Electric	Trial manufacture of Vb over 800 V





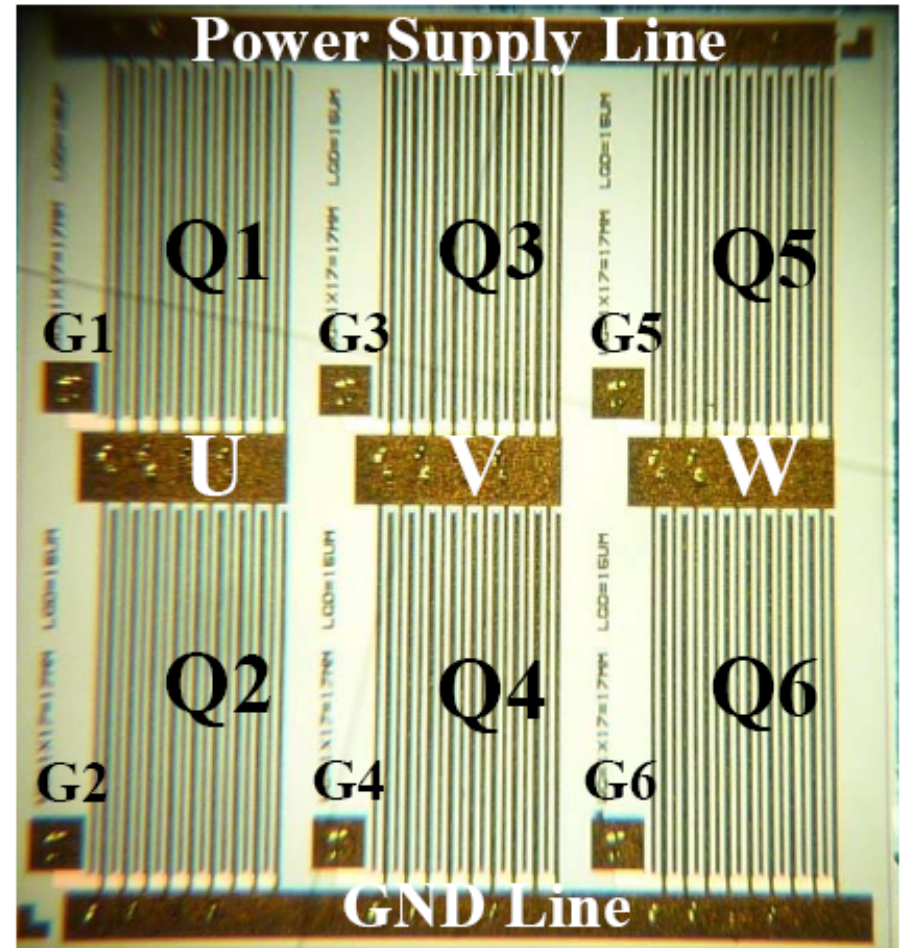
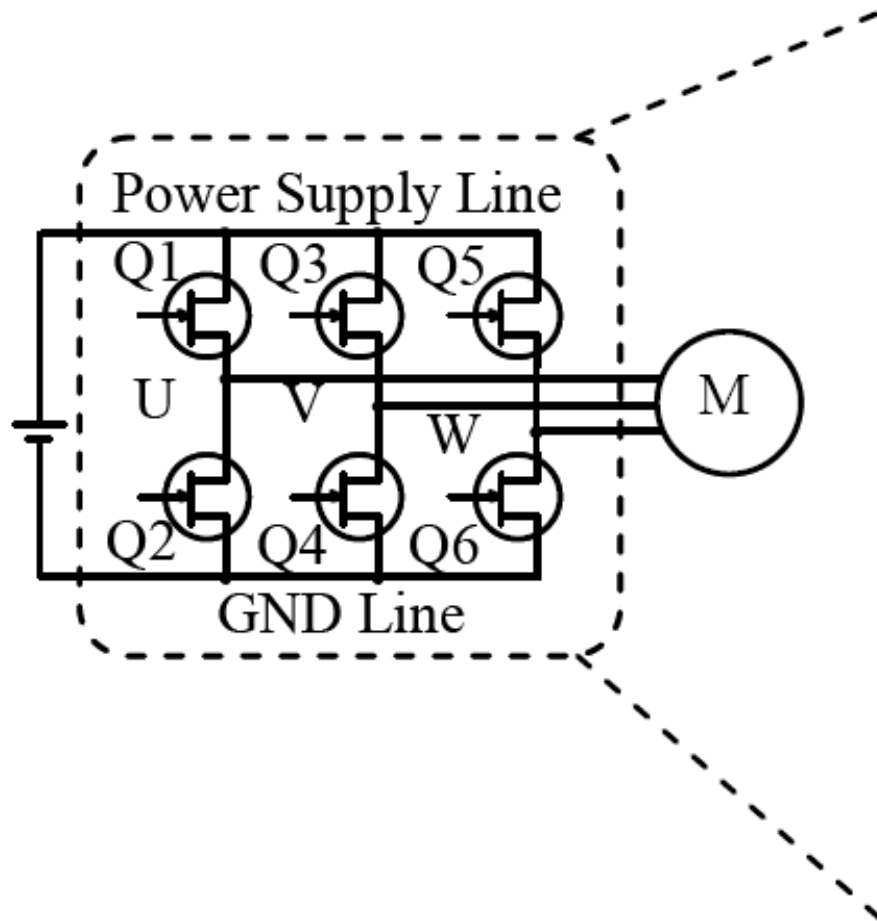


Fig.8 Chip photograph of fabricated GaN monolithic inverter IC in which 6 GITs are integrated.

2010年  
6/9 WED  
第1892号

Fujitsu THE SEMICONDUCTOR  
半導体産

この人に聞け!  
半導体トップ  
インタビュー

1000年度を振り返る。半導体業界の現状と今後の展望について、各社代表取締役社長にインタビュー。

**富士通セミコンダクター**  
代表取締役社長  
**岡田 晴基氏**

1000年度を振り返ると、半導体業界は厳しい状況に陥っています。特にメモリ市場は深刻な不況に陥っており、価格競争が激化しています。一方で、スマートフォンやタブレット端末の普及により、半導体需要は依然として堅調に推移しています。今後の展望としては、高付加価値な製品への開発と、グローバル市場での競争力強化が重要だと考えています。

半導体業界は、今後も成長を遂げる見込みです。特にスマートフォンやタブレット端末の普及により、半導体需要は依然として堅調に推移しています。また、自動車や産業用機器など、幅広い分野での半導体需要も伸びています。富士通は、これらの市場での競争力を強化し、高付加価値な製品を開発していく予定です。



岡田 晴基氏  
1000年度は、半導体業界が厳しい状況に陥りました。特にメモリ市場は深刻な不況に陥っており、価格競争が激化しています。一方で、スマートフォンやタブレット端末の普及により、半導体需要は依然として堅調に推移しています。今後の展望としては、高付加価値な製品への開発と、グローバル市場での競争力強化が重要だと考えています。

**GaN パワーを12年に量産開始**

富士通は、GaN パワー半導体の量産を開始しました。これは、半導体業界にとって重要なマイルストーンです。GaN パワー半導体は、高効率・高出力・高周波数動作が可能であり、自動車や産業用機器など、幅広い分野での応用が期待されています。富士通は、この技術を開発し、市場に供給することで、顧客のニーズに応えるとともに、半導体業界の発展に貢献していきます。

国内半導体大手12社の業績推移(単位:億円)

社名	09年度	10年1-3月	4-6月	7-9月	10年度
1 東芝	510,932	132,322	132,322	132,322	497,966
2 NECエレクトロニクス	7,027	1,946	1,480	1,855	5,269
3 パナソニック	▲666	▲224	▲266	▲43	▲964
4 ローム	5,259	964	1,143	1,115	▲684
5 エルピーダ	16,800	200	200	200	16,800
6 パナソニック	▲3,410	78	99	111	▲3,410
7 日立	▲1,474	▲423	8	8	▲1,474
8 パナソニック	▲929	767	767	767	▲929
9 ローム	2,978	724	40	86	106
10 日立	2,500	40	86	116	2,500
11 富士通セミコンダクター	3,903	660	787	90	▲600
12 三菱電機	1,279	39	39	39	1,279
13 日立	432	225	255	255	432
14 三菱電機	1,250	225	255	255	1,250
15 三菱電機	1,110	277	277	277	1,110
16 三菱電機	1,110	277	277	277	1,110
合計	49,545	10,040	12,214	12,040	▲1,689

上記は売上高、下は営業利益を示す。一部非公表。ロームの営業利益は全社ベース。シャープはLSIのみ。

半導体業界は、今後も成長を遂げる見込みです。特にスマートフォンやタブレット端末の普及により、半導体需要は依然として堅調に推移しています。また、自動車や産業用機器など、幅広い分野での半導体需要も伸びています。富士通は、これらの市場での競争力を強化し、高付加価値な製品を開発していく予定です。

半導体業界は、今後も成長を遂げる見込みです。特にスマートフォンやタブレット端末の普及により、半導体需要は依然として堅調に推移しています。また、自動車や産業用機器など、幅広い分野での半導体需要も伸びています。富士通は、これらの市場での競争力を強化し、高付加価値な製品を開発していく予定です。

- JPCA Show 2010特集 1.6~8面
- ラージエレクトロニクスショー 2010特集 16面
- 平坦化技術特集(CMP装置・材料) 1.10~15面

IC・半導体部品・電子部品・HDD  
創業1980/ベトナムによる充実した検査体制  
緊急品・ディスコン品専門商社  
世界を結ぶ半導体調達ネットワーク

www.aloman.co.jpで検索・見積りできます。  
手配代行・在庫買取・ロス・ダウンに利用ください。

アロマン株式会社 TEL:044(934)0034内  
FAX:044(934)0017

Para  
Sonic  
Ishiguro  
President of  
Panasonic

**GaN デバイス量産へ**  
パナソニック  
10年度度内有力

パナソニックは、GaN デバイスの量産を開始しました。これは、半導体業界にとって重要なマイルストーンです。GaN デバイスは、高効率・高出力・高周波数動作が可能であり、自動車や産業用機器など、幅広い分野での応用が期待されています。パナソニックは、この技術を開発し、市場に供給することで、顧客のニーズに応えるとともに、半導体業界の発展に貢献していきます。

高速 IC Package Substrate Bu

# ICパッケージ基板 バンパ検査装置

給排機付きバンパ検査装置  
**CVI-5020EX-RA**

個片状態のサブストレートバンパを高速度・高精度に検査する CVI シリーズに、給排機装置をドッキングした全自動バンパ検査装置です。高精度検査能力を保持しながら、1時間あたり約2000個を検査することが可能です。検査後、PASS品/NG品は仕分けられ、各々に収納されます。

株式会社高岳製作所  
エレクトロニクス装置事業本部  
http://www.takaoka.co.jp

## Server Power System Distribution from IBM

### 1. AC Distribution - 208/230/115V

- o Servers, Blade Servers, Workstations

### 2. 12V DC Distribution

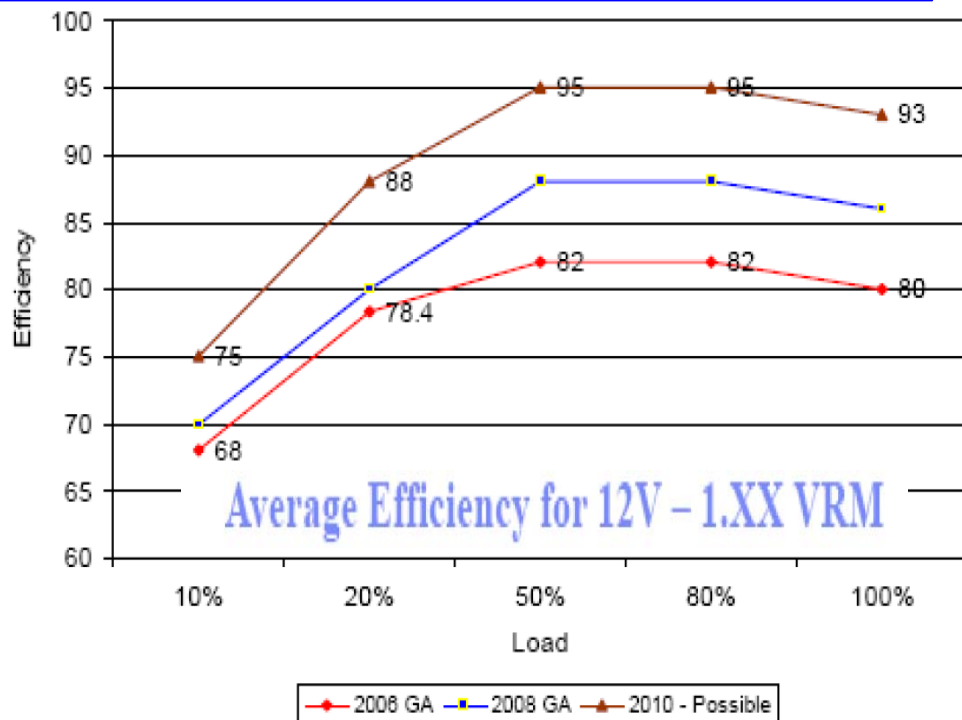
- o Blade Server Chassis, Low end and Midrange Servers, Workstations

### 3. 48V Distribution in a Rack

- o High End Server Applications

### 4. 350V DC Distribution in a Server Rack or a Rectifier Cabinet

- o Main Frame Servers



## International Workshop on Power Supply On Chip

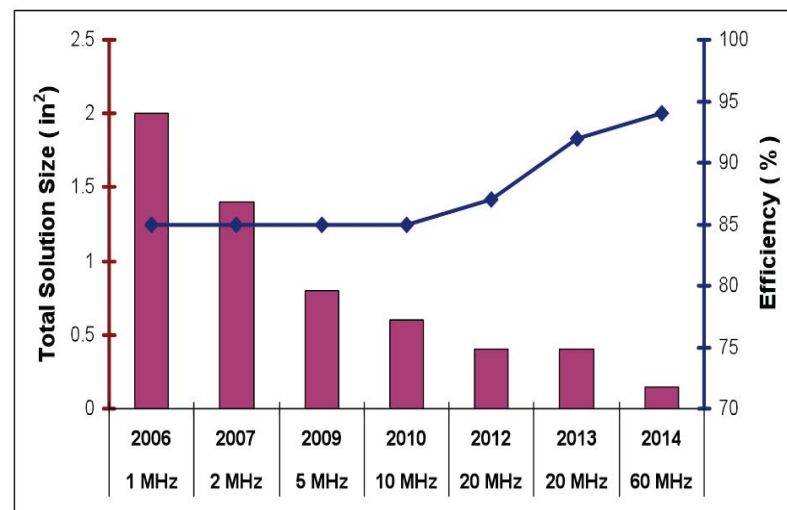
Sept 22nd - 24, 2008

October 13-15, 2010

Cork, Ireland

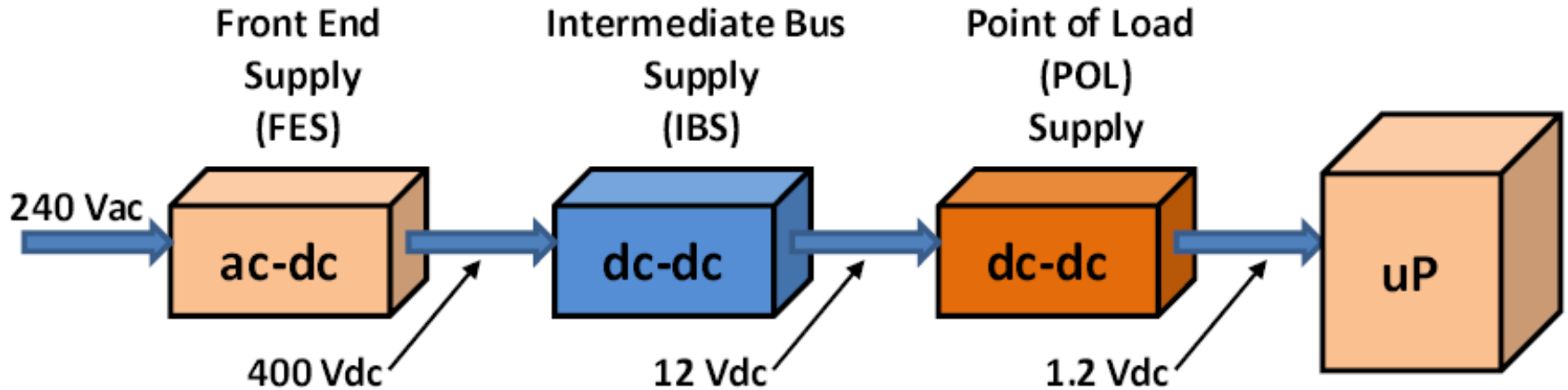
### Potential LV DC-DC Power Stage Roadmap

Optimized Performance – Without tradeoff



12Vin, 1.2Vout, 100A Based on Circuit Simulation

## AC - DC Power Efficiency Challenge by IBM September 2007



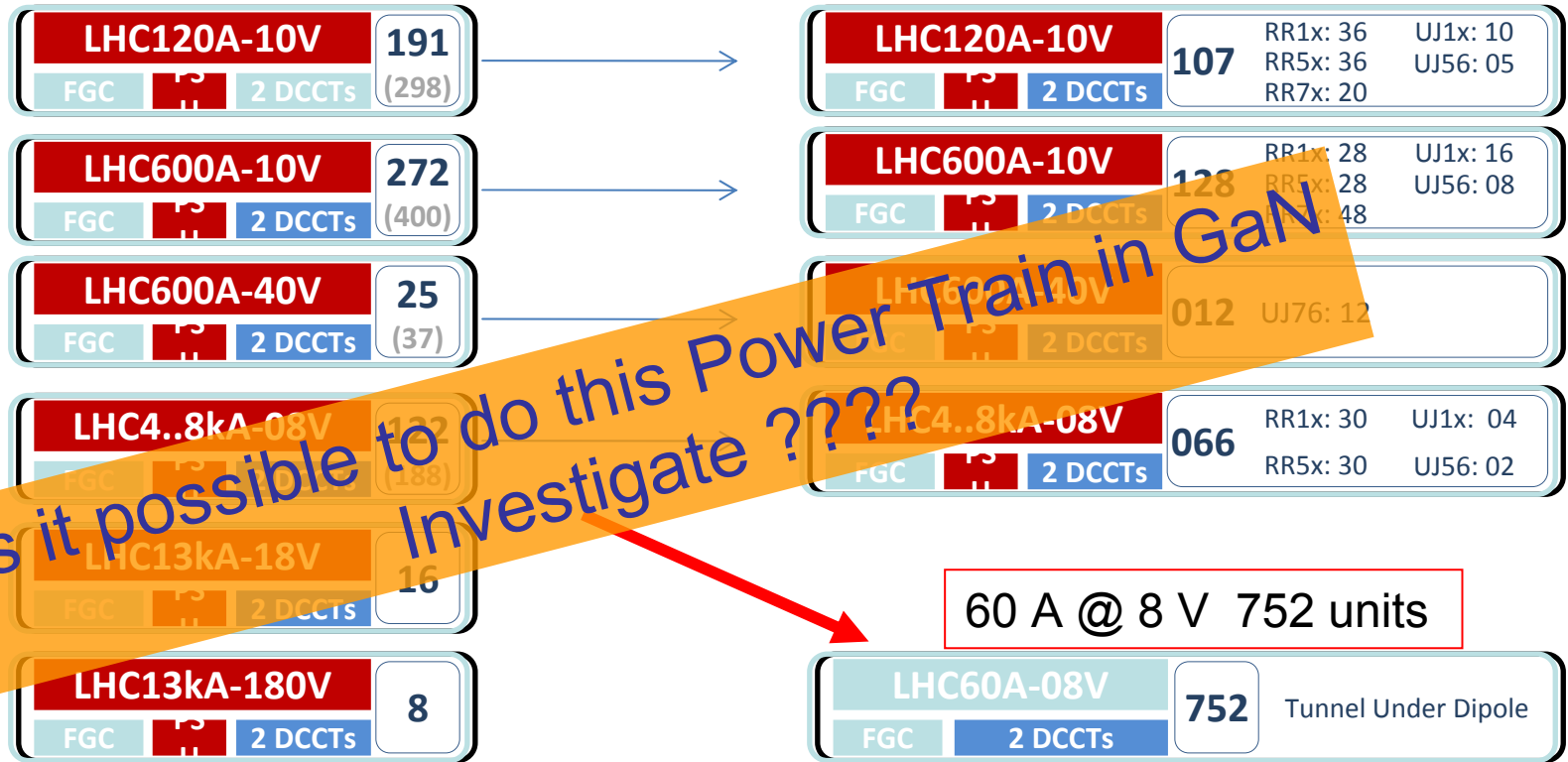
	FES	IBS	POL	Plug-to-Processor
Recent	93%	95%	88%	78%
Best Immediate	95%	98%	90%	84%
	IBM Challenge			<b>90%</b>
Needed	98%	98%	94%	<b>90%</b>

# CONVERTERS INSTALLED

CERN - Chamonix 2010 Report

## LHC CONVERTERS VS RADIATION [2010]

- Rad Tolerant Design *or* standard Design with low Rad sensitivity (safe components)
- Standard Design *and* Rad sensitivity unknown (too many components, sub-assemblies...)



## Review of the radiation tolerance of LHC power converters

Date: April 13 -14 2010

Reviewers: Bill Barcholet, Boeing (Email: bill.barcholet@boeing.com ). Jorgen Christiansen, CERN/PH-ESE (Email: Jorgen.christiansen@cern.ch ). Federico Faccio, CERN/PH-ESE (Email: Federico.Faccio@cern.ch ). Rémi Gaillard, Consultant (Email: gaillardremi@wanadoo.fr ). Bob Lambiase, BNL (Email: lambiase@bnl.gov ). Rick Tesarek, Fermilab (Email: tesarek@fnal.gov ). Kay Wittenburg, DESY (Email: kay.wittenburg@desy.de ). Prevented to be present in review meeting: Claudio Rivetta, SLAC (Email: rivetta@slac.stanford.edu ). Source of Material: <http://indico.cern.ch/internalPage.py?pagelId=0&confId=85477>

**Executive summary:** This review of the radiation tolerance of the power converters for the LHC accelerator concludes that the current power converters at their current locations introduces a significant risk for the reliable running of the LHC at high luminosities. At nominal LHC luminosity it can be estimated that radiation induced transient and destructive failures in the power converters might seriously limit the beam availability for physics.

For the initial 1st physics run (2010 -2011), at low luminosity, the currently implemented shielding improvements is estimated to allow the current power converters to have sufficient reliability. There is in practice no other alternative, as no major improvements can be introduced during the running period.

**For the planned 2nd physics run (2013-2014), at increased luminosity and increased energy, radiation induced failures can become a limiting factor for the running of the accelerator (directly dependent on luminosity).** Because of the limited time available only minor incremental improvements can in practice be implemented to improve this (relocation, improved shielding and minor power converter modifications). It is proposed to perform a paper vulnerability analysis of the current power converters as soon as possible. Part-level radiation tests of identified high risk components can then be made to determine where such improvements can be implemented to reduce the global risk.

For the long term running at full nominal luminosity the current power converters can be expected to become a serious limitation for the effective running of the accelerator. They should therefore be replaced by new radiation tolerant converters or the current converters must be relocated to areas without radiation. The design and production of new radiation tolerant power supplies will require a significant time and should therefore be started as early as possible.

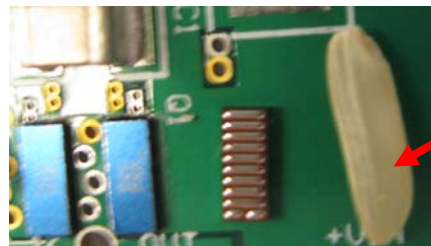
# Conclusions

- The power distribution needs of HEP detectors require new solutions/technologies to meet power and environmental requirements.
- DC/DC (Buck) Converters are potential solutions for these needs.
- The environment requires that these converters operate in high radiation environments and high magnetic fields at high switching frequencies in a small size/mass package.
- Target technologies for the switches are radiation hard GaN and 0.25  $\mu\text{m}$  LDMOS. High frequency controllers driving small sized nonmagnetic/air core inductors are also required.
- Many of these components have been tested and now need integration to produce a working prototype. This is the next step in our R&D program.

# What can be achieved by this Development ?

- ❖ Current Reduction from Power Supply by DC-DC near Load  
Losses  $> \text{Current}^2 \times \text{Resistance}$
- ❖ Silicon  $\div 10$  Current Reduction 5 Oodle  $> 0.5$  Oodle  
*Power Loss reduced by 100*
- ❖ GaN  $\div 50$  Current Reduction 5 Oodle  $> 0.1$  Oodle  
*Power Loss reduced by 2500*  
Power Converters for Beam Line usage ??

## Thermal Challenge



A grain of Basmati Rice  
4 watts

GaN FETs  
40 V 33 A 4m $\Omega$

FET Solder side



A polar bear stands on a vast, flat expanse of snow and ice. To the left, there is a large, irregular hole in the ice, revealing dark water. The bear is positioned in the center-left, facing right. Its shadow is cast long and dark to the left. A series of tracks leads from the bottom right towards the bear. The overall scene is desolate and cold.

# Working on Power Supply Is not Glamorous

Top of the World is Cool but lonely!  
Let us keep it cool with highly efficient PS  
Swimming is Great at the North Pole

**More Details:** <http://shaktipower.sites.yale.edu/>